

ALCF INCITE GPU Hackathon May 20-22, 2025

Performance Analysis of GPU-accelerated Applications with HPCToolkit

John Mellor-Crummey Rice University

Outline

- Introduction to HPCToolkit performance tools
 - -Overview of HPCToolkit components and their workflow
 - -HPCToolkit's graphical user interfaces
- Analyzing the performance of GPU-accelerated codes with HPCToolkit
 - Slides: Exawind (AMReX)
 - -Slides: LAMMPS at Exascale (Kokkos)
 - -Demo: GAMESS (OpenMP)
 - -Hands-on:
 - -Explore available performance databases for a set of applications
 - -Collect and explore some measurements for some prepared examples



Linux Foundation's HPCToolkit Performance Tools

Collect profiles and traces of unmodified parallel CPU and GPU-accelerated applications

Understand where an application spends its time and why

call path profiles associate metrics with application source code contexts

analyze instruction-level performance within GPU kernels and attribute it to your source code

hierarchical traces to understand execution dynamics

Parallel programming models

across nodes: MPI, SHMEM, UPC++, ...

within nodes: OpenMP, Kokkos, RAJA, HIP, DPC++, Sycl, CUDA, OpenACC, ...

Languages

C, C++, Fortran, Python, ...

Hardware

CPU cores and GPUs within a node CPU: x86 64, Power, ARM

GPU: NVIDIA, AMD, Intel



Why HPCToolkit?

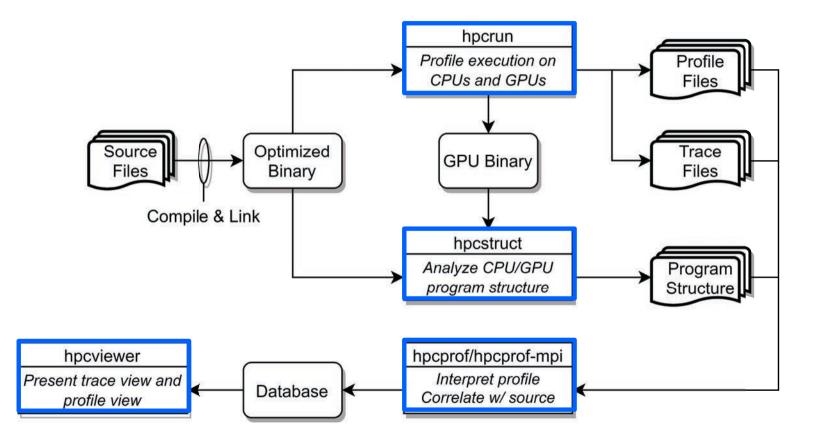
- Measure and analyze performance of CPU and GPU-accelerated applications
- Easy: profile unmodified application binaries
- Fast: low-overhead measurement
- · Informative: understand where an application spends its time and why
 - -call path profiles associate metrics with application source code contexts
 - -optional hierarchical traces to understand execution dynamics
- Broad audience
 - -application developers
 - -framework developers
 - -runtime and tool developers
- Unlike vendor tools works with a wide range of CPUs and GPUs



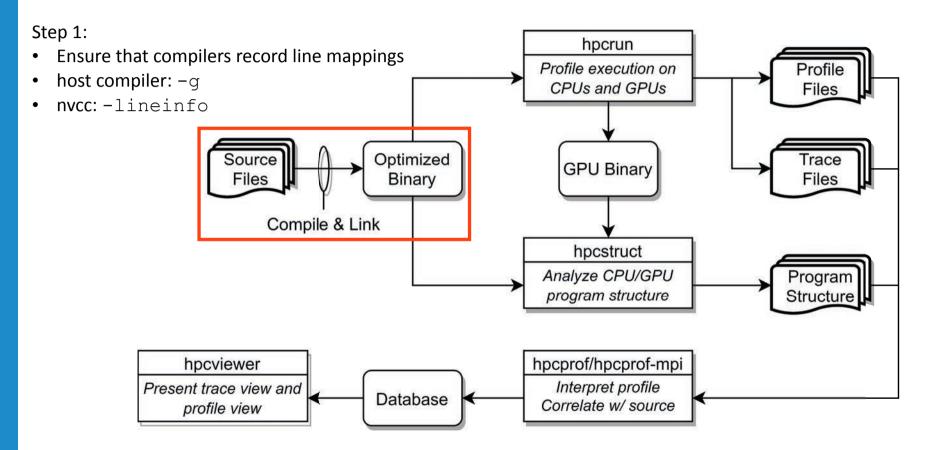
How does HPCToolkit Differ from Vendor Tools?

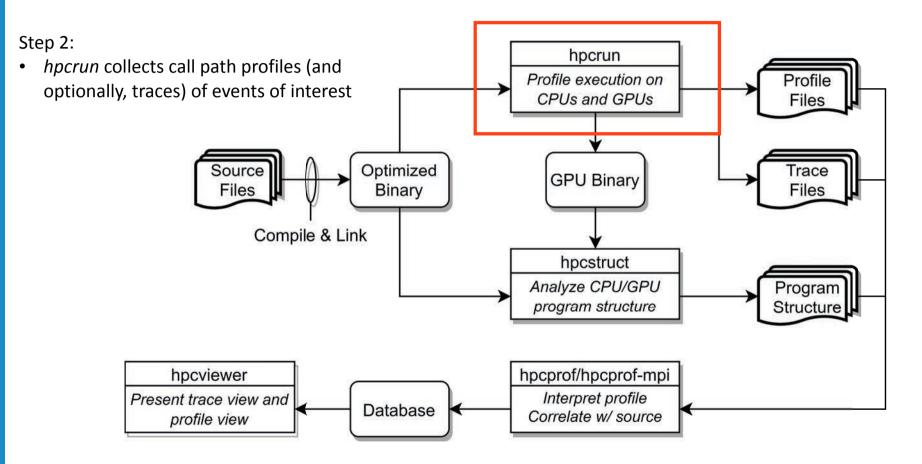
- NVIDIA NSight Systems
 - -tracing of CPU and GPU streams
 - -analyze traces when you open them with the GUI
 - long running traces are huge and thus extremely slow to analyze, limiting scalability
 - -designed for measurement and analysis within a node
- NVIDIA NSight Compute
 - -detailed measurement of kernels with counters and execution replay
 - -very slow measurement
 - -flat display of measurements within GPU kernels
- Intel VTune: designed for analysis of performance on a single node
- AMD Omtitrace: designed for analysis of performance on a single node
- HPCToolkit
 - -supports more scalable tracing than vendor tools
 - measure exascale executions across many GPUs and nodes
 - ---scalable, parallel post-mortem analysis vs. non-scalable in-GUI analysis
 - -detailed reconstruction of estimates for calling context profiles within GPU kernels











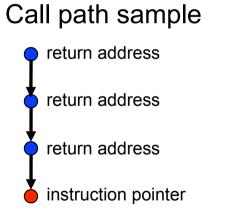


Measurement of CPU and GPU-accelerated Applications

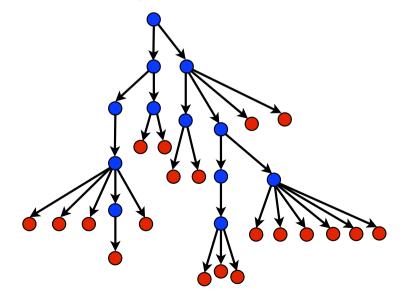
- Sampling using Linux timers and hardware counter overflows on the CPU
- Callbacks when GPU operations are launched and (sometimes) completed
- Event stream for GPU operations
- PC Samples: NVIDIA (in progress: AMD, Intel)
- Binary instrumentation of GPU kernels on Intel GPUs for fine-grain measurement

Call Stack Unwinding to Attribute Costs in Context

- Unwind when timer or hardware counter overflows
 - -measurement overhead proportional to sampling frequency rather than call frequency
- Unwind to capture context for events such as GPU kernel launches

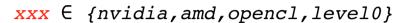


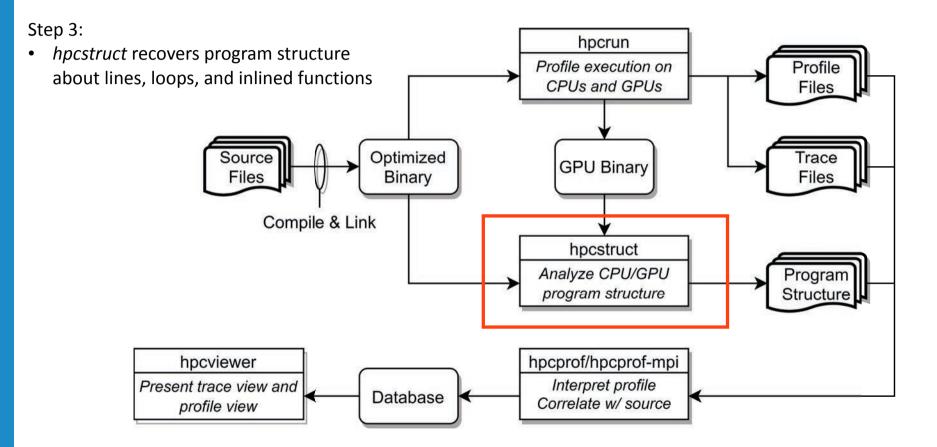
Calling context tree



hpcrun: Measure CPU and/or GPU activity

- GPU profiling
 - -hpcrun -e gpu=xxx <app>
- GPU PC sampling (NVIDIA GPU only) —hpcrun -e gpu=nvidia,pc <app>
- CPU and GPU Tracing (in addition to profiling)
 —hpcrun -e CPUTIME -e gpu=xxx -tt <app>
- Use hpcrun with MPI on Polaris or Aurora —mpiexec _n <ranks> ... hpcrun _e gpu=xxx <app>





hpcstruct: Analyze CPU and GPU Binaries Using Multiple Threads

• Usage

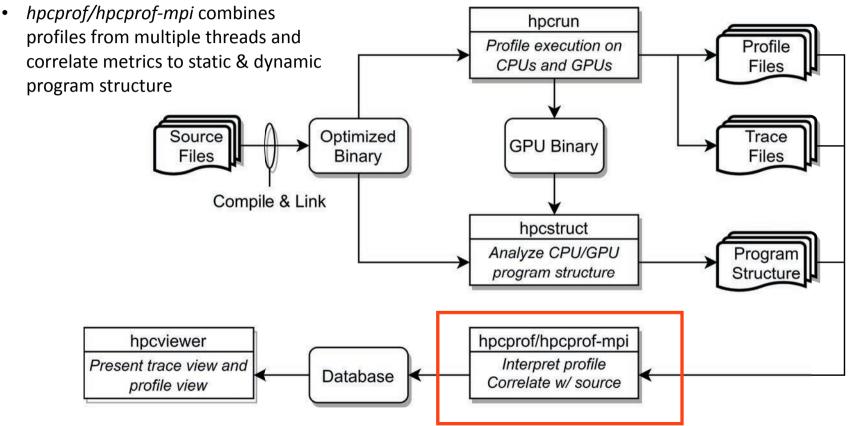
hpcstruct [--gpucfg yes] <measurement-directory>

- What it does
 - Recover program structure information
 - Files, functions, inlined templates or functions, loops, source lines
 - In parallel, analyze all CPU and GPU binaries that were measured by HPCToolkit
 - -typically analyze large application binaries with 16 threads
 - -typically analyze multiple small application binaries concurrently with 2 threads each
 - Cache binary analysis results for reuse when analyzing other executions

NOTE: --gpucfg yes needed only for analysis of GPU binaries for interpreting PC samples on NVIDIA GPUs



Step 4:



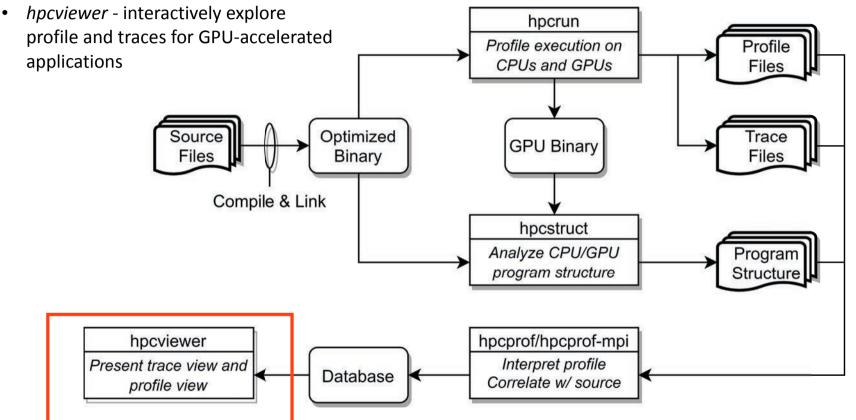
hpcprof/hpcprof-mpi: Associate Measurements with Program Structure

- Analyze data from modest executions with multithreading (moderate scale)
 hpcprof <measurement-directory>
- Analyze data from large executions with distributed-memory parallelism + multithreading (large scale)

```
mpiexec -n ${NODES} --ppn 1 -depth=128 \
    hpcprof-mpi <measurement-directory>
```

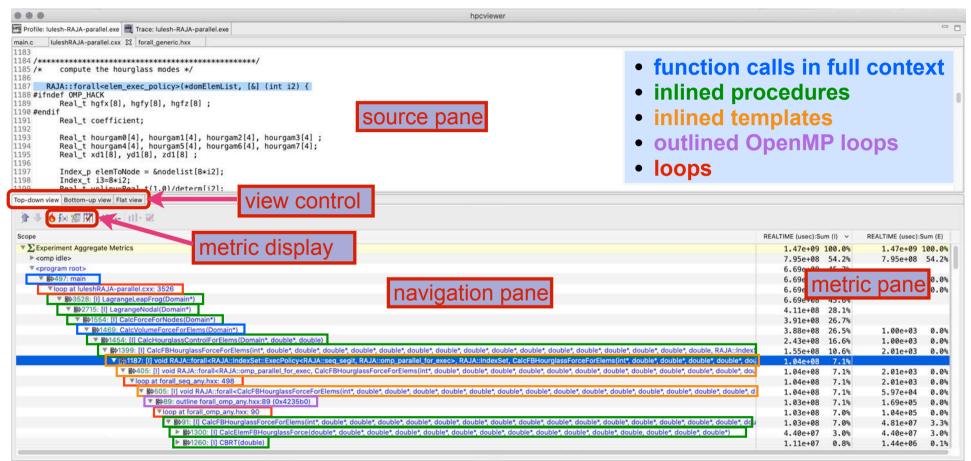


Step 4:





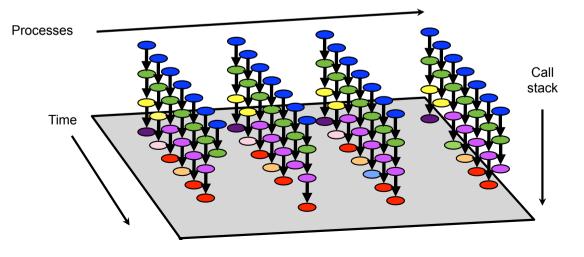
Code-centric Analysis with hpcviewer



RICE

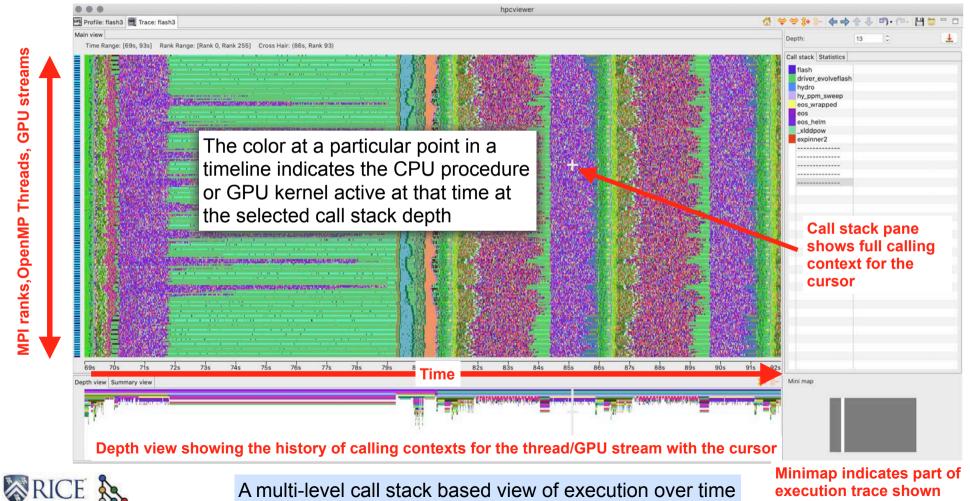
Understanding Temporal Behavior

- Profiling compresses out the temporal dimension
 - -Temporal patterns, e.g. serial sections and dynamic load imbalance are invisible in profiles
- What can we do? Trace call path samples
 - -N times per second, take a call path sample of each thread
 - -Organize the samples for each thread along a time line
 - —View how the execution evolves left to right
 - —What do we view? assign each procedure a color; view a depth slice of an execution





Time-centric Analysis with hpcviewer



A multi-level call stack based view of execution over time

Minimap indicates part of execution trace shown

Case Studies

- ExaWind
- GAMESS (OpenMP)
- Quicksilver (CUDA)
- LAMMPS (Kokkos) at exascale



ExaWind: Wakes from Three Turbines over Time

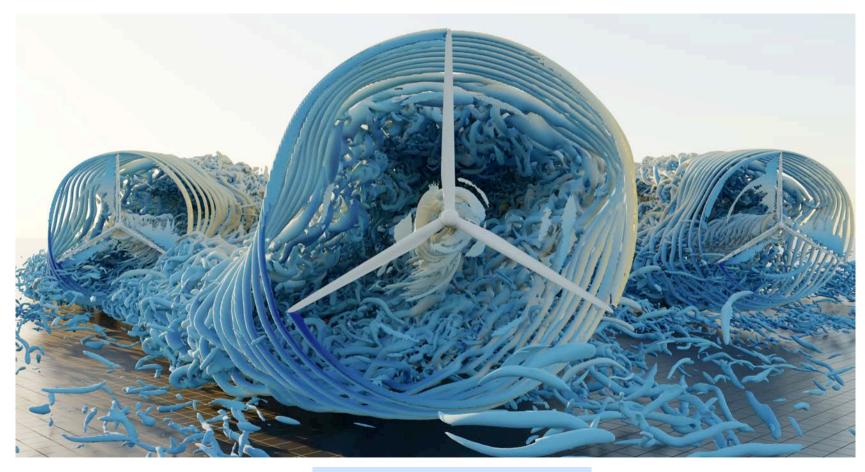




Figure credit: Jon Rood, NREL

ExaWind: Visualization of a Wind Farm Simulation

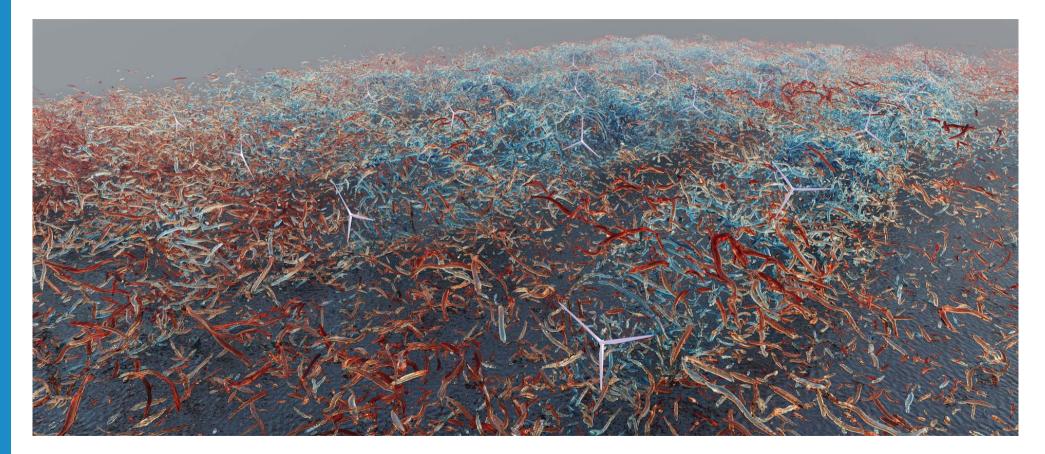




Figure credit: Jon Rood, NREL

ExaWind: Execution Traces on Frontier Collected with HPCToolkit

Traces on roughly ~70K MPI ranks for ~17minutes

Before: MPI waiting (bad), shown in red

After: MPI overhead negligible*

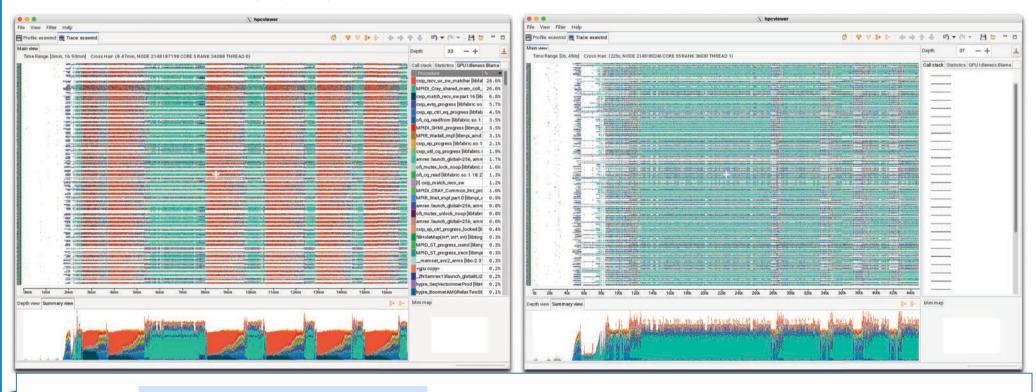


Figure credits: Jon Rood, NREL

*replaced non-blocking send/recv with ialltoallv

ExaWind Testimonials for HPCToolkit

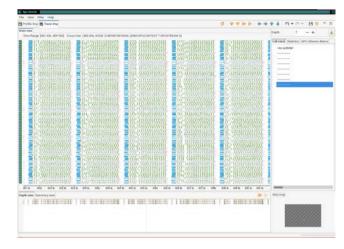
I just wanted to mention we've been using HPCToolkit a lot for our ExaWind application on Frontier, which is a hugely complicated code, and your profiler is one of the only ones we've found that really lets us easily instrument and then browse what our application is doing at runtime including GPUs. As an example, during a recent hackathon we had, we improved our large scale performance by 24x by understanding our code better with HPCToolkit and running it on 1000s of nodes while profiling. We also recently improved upon this by 10% for our total runtime.

- Jon Rood NREL (5/31/2024)

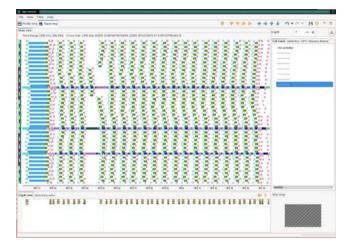
One big thing for us is that we can't overstate how complicated ExaWind is in general, and how complicated it is to build, so finding out that HPCToolkit could easily profile our entire application without a ton of instrumentation during the build process, and be able to profile it on a huge amount of Frontier with line numbers and visualizing the trace was really amazing to us.

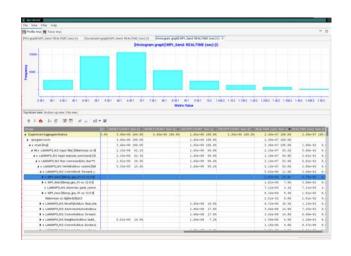
- Jon Rood NREL (6/3/2024)



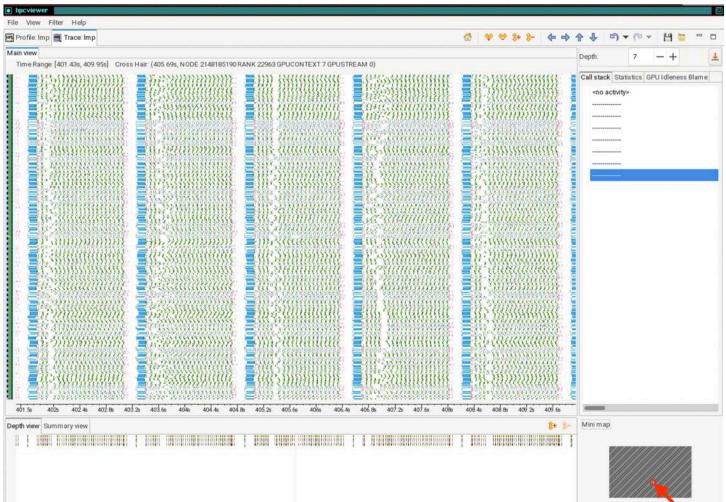


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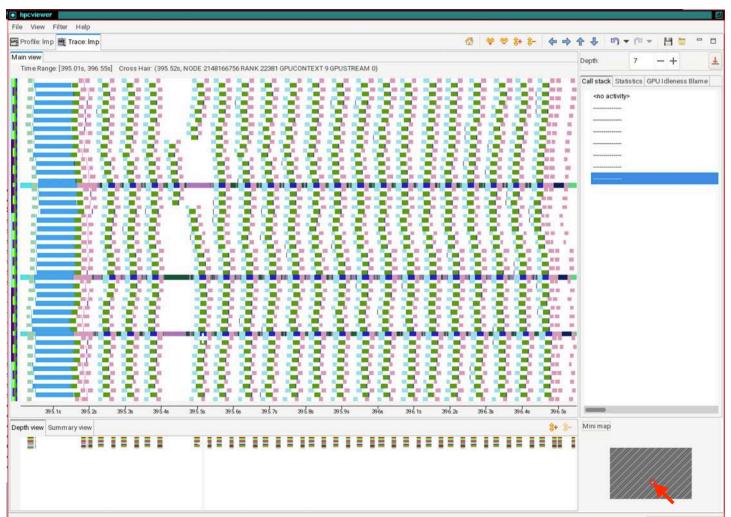




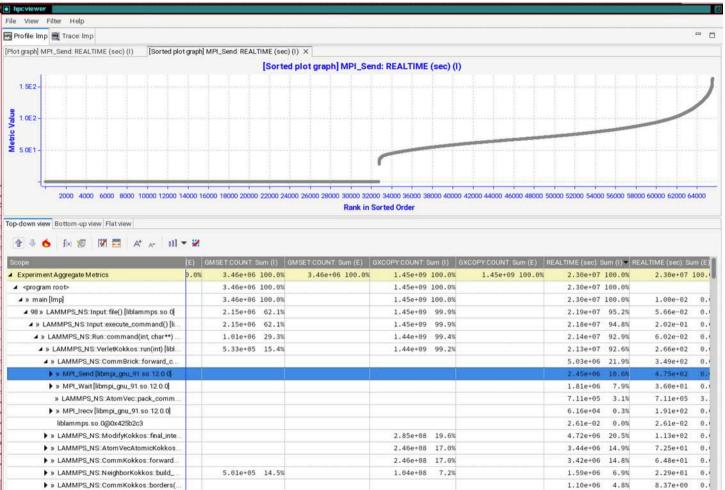












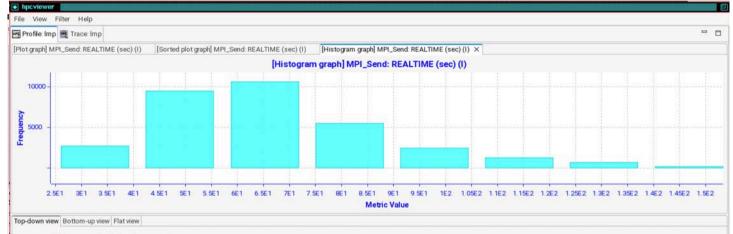
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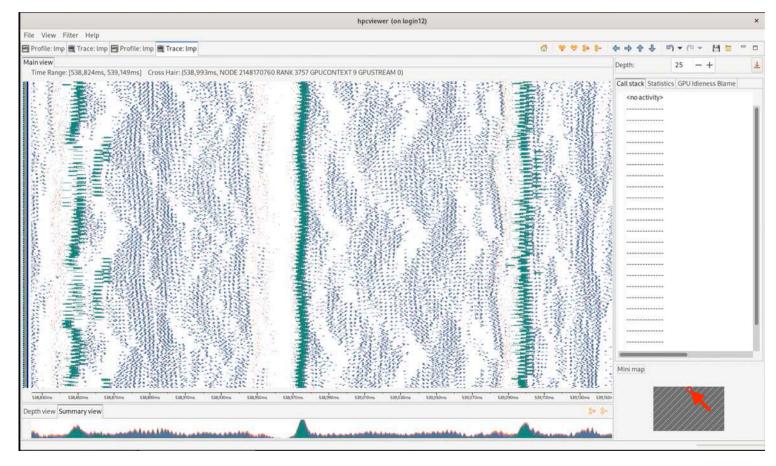
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Scope	(E)	GMSET:COUNT: Sum (I)	GMSET COUNT. Sum (E)	GXCOPY:COUNT: Sum (I)	GXCOPY COUNT: Sum (E)	REALTIME (sec): Sum (I)	REALTIME (sec): S	um (E)
 Experiment Aggregate Metrics 	9.0%	3.46e+06 100.0%	3.46e+06 100.0%	1.45e+09 100.0%	1.45e+09 100.0%	2.30e+07 100.0%	2.30e+07	100.
<pre>▲ <program root=""></program></pre>		3.46e+06 100.0%		1.45e+09 100.0%		2.30e+07 100.0%		
🖌 » main [Imp]		3.46e+06 100.0%		1.45e+09 100.0%		2.30e+07 100.0%	1.00e-02	θ.
▲ 98 » LAMMPS_NS:Input:file() [liblammps.so.0]		2.15e+06 62.1%		1.45e+09 99.9%		2.19e+07 95.2%	5.66e-02	0.
 LAMMPS_NS Input execute_command() [li 		2.15e+06 62.1%		1.45e+09 99.9%		2.18e+07 94.8%	2.02e-01	θ.
A » LAMMPS_NS::Run::command(int, char**)		1.01e+06 29.3%		1.44e+09 99.4%		2.14e+07 92.9%	6.02e-02	θ.
▲ » LAMMPS_NS::VerletKokkos::run(int) [libl		5.33e+05 15.4%		1.44e+09 99.2%		2.13e+07 92.6%	2.66e+02	θ.
A » LAMMPS_NS_CommBrick_forward_c						5.03e+06 21.9%	3.49e+02	θ.
MPI_Send [libmpi_gnu_91 so 12.0.0]						2.45e+06 10.6%	4:75e+02	θ,
» MPI_Wait [libmpi_gnu_91.so. 12.0.0]						1.81e+06 7.9%	3.60e+01	0.
» LAMMPS_NS_AtomVec_pack_comm						7.11e+05 3.1%	7.11e+05	3.
MPI_Irecv [libmpi_gnu_91 so 12.0.0]						6.16e+04 0.3%	1.91e+02	0.
liblammps.so.0@0x425b2c3						2.61e-02 0.0%	2.61e-02	θ.
LAMMPS_NS:ModifyKokkos:final_inte				2.85e+08 19.6%		4.72e+06 20.5%	1.13e+02	0.1
» LAMMPS_NS:AtomVecAtomicKokkos				2.46e+08 17.0%		3.44e+06 14.9%	7.25e+01	0.
» LAMMPS_NS::CommKokkos::forward				2.46e+08 17.0%		3.42e+06 14.8%	6.48e+01	0.
LAMMPS_NS:NeighborKokkos:build		5.01e+05 14.5%		1.04e+08 7.2%		1.59e+06 6.9%	2.29e+01	0.0
LAMMPS_NS_CommKokkos_borders(1.10e+06 4.8%	8.37e+00	θ.
k				1 20-107 0.00		0.07-105 0.54	0.1000	



LAMMPS on Frontier: 8K nodes, 64K MPI ranks + 64K GPU tiles

Kernel duration of microseconds





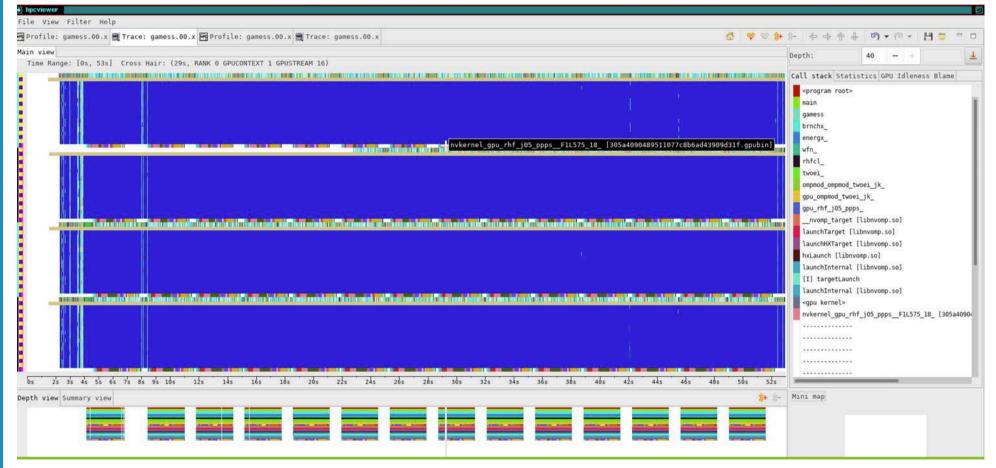
Case Study: GAMESS

- General Atomic and Molecular Electronic Structure System (GAMESS)
 - -general ab initio quantum chemistry package
- Calculates the energies, structures, and properties of a wide range of chemical systems
- Experiments
 - GPU-accelerated nodes at a prior Perlmutter hackathon
 - Single node with 4 GPUs
 - Five nodes with 20 GPUs

Perlmutter node at a glance

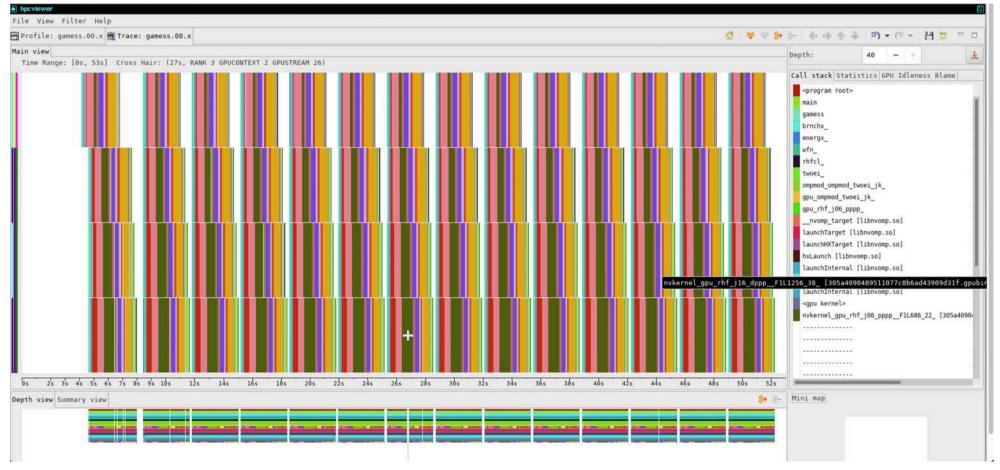
AMD Milan CPU 4 NVIDIA A100 GPUs 256 GB memory



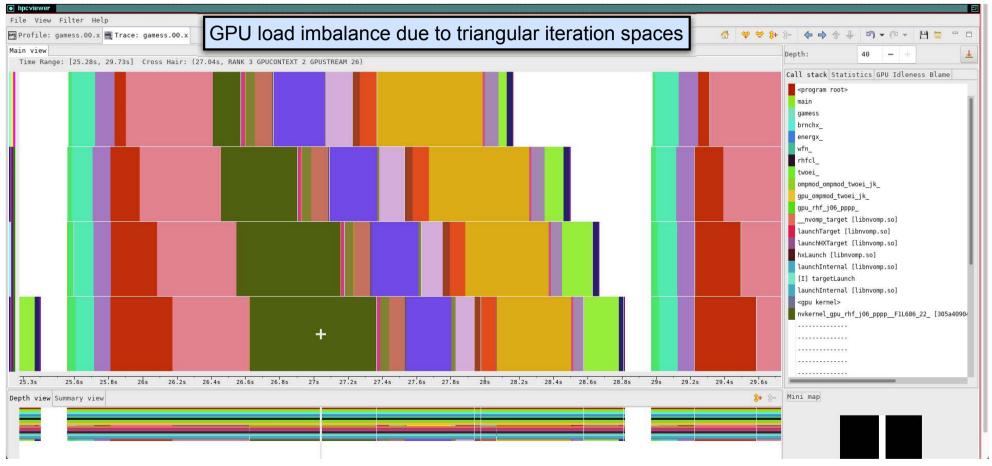




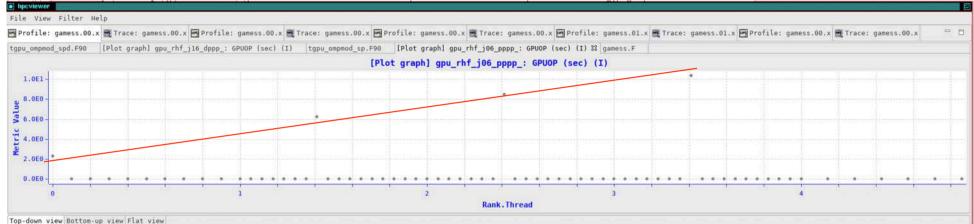
Profile: gamess.00.x 🗮 Trace: gamess.00.x 🔤 Profile: gamess.00.x 🗮 Trace: ga	mess.00.x 편 Profile: gamess.00.x 💐 Trace: gamess.00.x 편 Profile: ga	mess.01.x 🖷 Trace: gamess.01.x 🗃 Profile: gamess.00.x 🖷 Trace: gamess.00.x
	Select rank to display	■
ain view	Check all Uncheck all Regular expression	Depth: 40 - +
Time Range: [0s, 53s] Cross Hair: (42s, RANK 0 THREAD 10)		Depth. 40 -
	Filter: GPU	Call stack Statistics GPU Idleness Blame
	Ranks or threads	<pre></pre>
	1 RANK 0 GPUCONTEXT 1 GPUSTREAM 16	threadPoolEntryPoint [libnvomp.so]
	2 RANK 1 GPUCONTEXT 2 GPUSTREAM 26	hxiExecuteHostTreeBarrierWithTasks [libnvomp
	3 RANK 2 GPUCONTEXT 2 GPUSTREAM 26	[I] executeHostTreeBarrier
	4 RANK 3 GPUCONTEXT 2 GPUSTREAM 26	[I] waitForNeighborThreads
		http://www.artennasia/www.artennasia/www.artennasia/
		syscall [libc-2.31.so]
and the second		and a set of the set o
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ter and a second statement of a second statement of a second statement of a second statement of second second s		
	Cancel OK	
05 25 35 45 55 65 75 85 95 105 125 145 165 185 205 22	s 24s 26s 28s 30s 32s 34s 36s 38s 40s	42s 44s 46s 48s 50s 52s
epth view Summary view		8+ jj- Mini map











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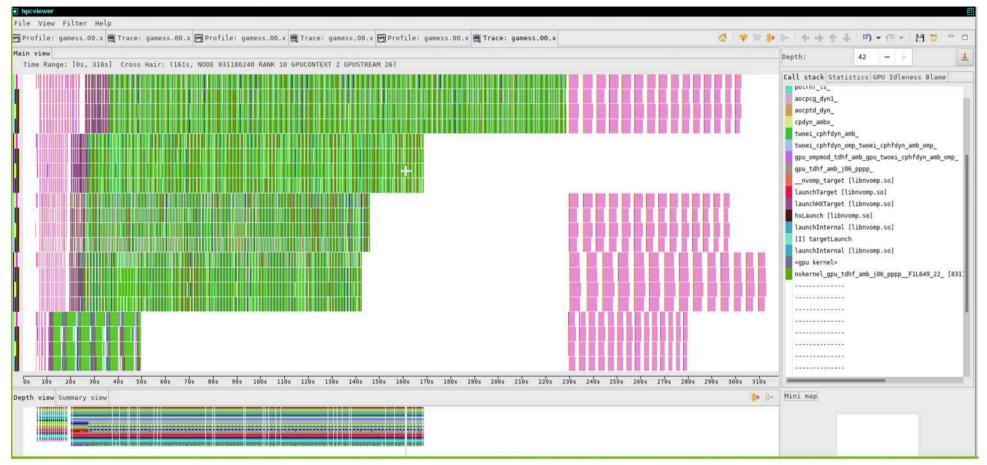
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Scope	REALTINE (sec): Sum (I)		GPUOP (sec): Sum (I) 📼	GPUOP (sec); Sum (E)	GKER (sec): Sum (I)		GXCOPY (sec): Sum (I)	
▲ Experiment Aggregate Metrics	3.48e+03 100.0	% 3.48e+03 100.0	1.66e+02 100.0%	1.66e+02 100.0%	1.66e+02 100.0%	1.66e+02 100.0%	2.64e-01 100.0%	2.64e-01 10
▲ <program root=""></program>	2.04e+02 5.5	9%	1.66e+02 100.0%		1.66e+02 100.0%		2,64e-01 100.0%	
▲ » main [gamess.00.x]	2.04e+02 5.5	24	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
🖌 » gamess	2.04e+02 5.5	Pa	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
▲ 772 » brnchx_	2.03e+02 5.8	Pi	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
▲ 1095 energx	2.03e+02 5.8	5%	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
loop at gamess.F: 1316	2.03e+02 5.8	5%	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
▲ loop at gamess.F: 1436	1.97e+02 5.3	7%	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
🖌 loop at gamess.F: 1436	1.97e+02 5.3	7%	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
🖌 1440 🝺 wfn_	1.97e+02 5.1	7%	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
▲ loop at gamess.F: 2645	1.97e+02 5.3	7%	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
∠ 2568 rhfcl_	1.97e+02 5.7	1.04e-02 0.0	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
loop at rhfuhf.f: 2678	1.96e+02 5.7	% 5.25e-03 0.0	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
▲ loop at rhfuhf.f: 2723	1.96e+02 5.0	5%	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
▲ loop at rhfuhf.f: 2723	1.96e+02 5.0	5%	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
▲ 2859 » twoei_	1.69e+02 4.5	9%	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
▲ 3994 » ompmod_ompmod_twoei_jk_	1.69e+02 4.5	9%.	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
⊿ 246 w gpu_onpmod_twoei_jk_	1.69e+02 4.5	24	1.66e+02 100.0%		1.66e+02 100.0%		2.64e-01 100.0%	
656 s gpu_rhf_j16_dppp_	2.98e+01 0.5	n 1.00e-02 0.0	2.97e+01 17.9%		2.97e+01 17.9%		1.07e-02 4.1%	
\$89 gpu_rhf_106_pppp_	2.74e+01 0.8	5,00e-03 0.0	A 2.74e+01 16.5%		2.74e+01 16.5%		8.56e-03 3.2%	
N. 444 1.7 (AP	a. re. a. a.	w'	• • • • • • • •	L			0.00.00.00	

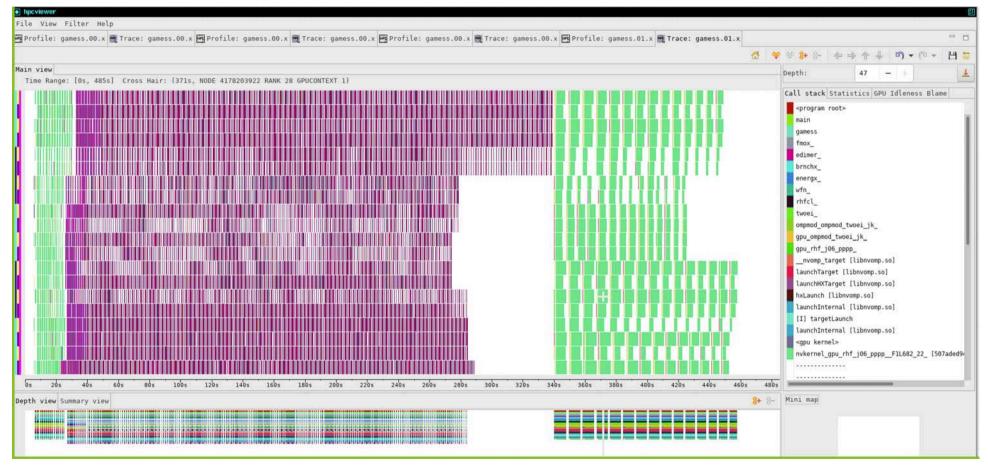


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View	Depth: 42 - +
me Range: [0s, 318s] Cross Hair: (260s, NODE 931177797 RANK 19 THREAD 0)	
	Call stack Statistics GPU Idleness Blame
	twoei
	ompmod_ompmod_twoei_jk_
	gpu_ompmod_twoei_jk_
	gpu_rhf_j06_pppp_
	nvomp_target [libnvomp.so]
	launchTarget [libnvomp.so]
	launchHXTarget [libnvomp.so]
	hxLaunch [libnvomp.so]
	launchInternal [libnvomp.so]
	[I] targetLaunch
	launchInternal [libnvomp.so] libcuda.so.515.48.07@0x2e8ca6
	libcuda.so.515.48.07@0x124400
	libcuda.so.515.48.07@0x48d0d5
	libcuda.so.515.48.07@0x25985b
	libcuda.so.515.48.07@0x16ebf6
	libcuda.so.515.48.07@0x4b612e
	libcuda.so.515.48.07@0x4b42ee
	libcuda.so.515.48.07@0x1501cd
	libcuda.so.515.48.07@0x3ce710
165 265 365 465 565 665 765 865 965 1005 1105 1205 1305 1405 1505 1605 1705 1805 1905 2005 2105 2205 2305 2405 2505 2605 2705 2805 2905	300s 310s
View Summary view	👔 ji- Mini map



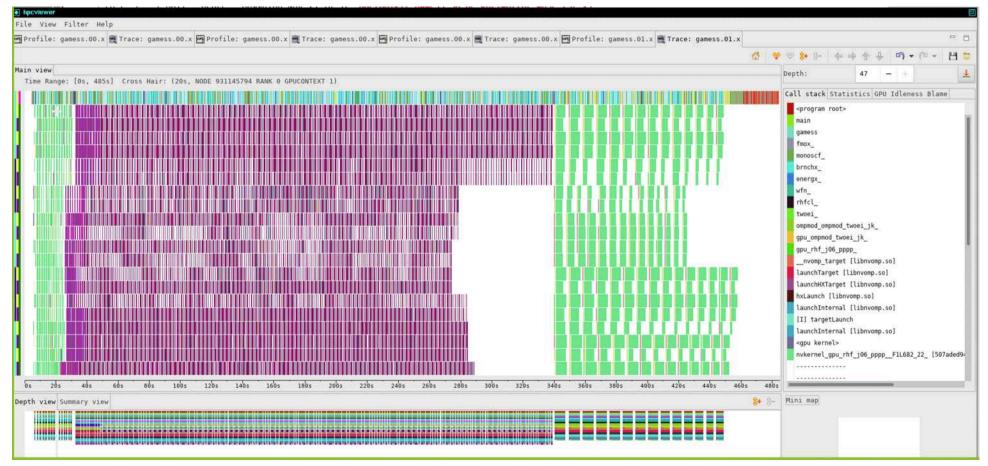






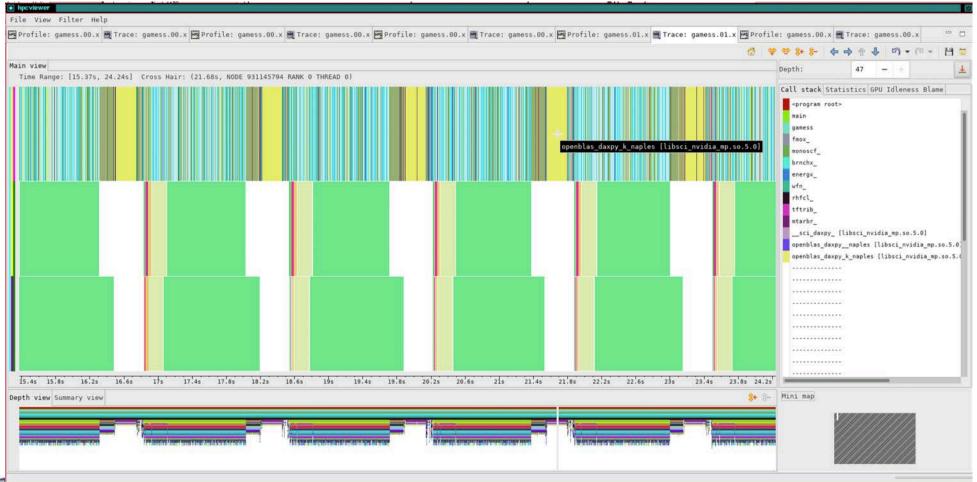
GAMESS improved with better manual distribution of work in input

RICE



GAMESS improved adding Rank 0 Thread 0 to GPU streams

RICE



RICE 🛼

1 CPU Stream, 2 GPU Streams: 6 Iterations

hpcvie		1
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mthlib.	f 🖂	
1053 1054 C	END	
1055 C*MO	DULE MTHLIB +DECK MTARBR	
1056 1057 C	SUBROUTINE MTARBR(A, NA, B, MB, AB, NAB, INCA)	
1058 1059 C	use omp_lib	
1060 1061 C	IMPLICIT DOUBLE PRECISION(A.H.O-Z)	
1062 1063 C	DIMENSION A(*),B(NA,MB),AB(NAB,MB)	- 18
1064 1065 C	PARAMETER (ZERO=0.0D+90)	
1966 C* 3 1067 C*	1 0CT 1979	
1068 C*FU 1069 C*	NCTION - TO MULTIPLY SYMMETRIC MATRIX A TIMES RECTANGULAR MATRIX B AND GET RECTANGULAR MATRIX AB	
1070 C* 1071 C*PA		
1072 C* 1073 C*	A - THE INPUT REAL SYMMETRIC MATRIX OF ORDER NA STORED IN SYMMETRIC STOAGE MODE.	
1074 C*	8 - THE INPUT REAL NA BY MB RECTANGULAR MATRIX NA - THE ORDER OF MATRIX A	
1076 C*	MB - THE COLUMN DIMENSION OF MATRICES B AND AB	
1077 C* 1978 C*	AB - THE OUTPUT PRODUCT NA BY MB MATRIX NAB - THE INPUT ROW DIMENSION OF MATRIX AB	
1979 C* 1980 C*	INCA - ADDRESS DIFFERENCE OF TWO ADJACENT ELEMENTS OF A	
1081 1082 C	INC=INCA	
1083 C 1084 C	PROCESS DIAGONAL ELEMENTS OF INPUT MATRIX A	
1085	1)=1-NC D0 120 1=1.4A	
1087	I)=I)+[*]NC A]]=A[]]	
1089	D0 110 K=1,M8 A0(I,K)=A1349(I,K)	
1891 11	0 CONTINUE 0 CONTINUE	
1093 1094 C	TF(MA.ED.1) RETURN	
1095 C 1095 C	PROCESS OFF-DIAGONAL ELEMENTS OF INPUT MATRIX A	
1097	IJ=1-INC D0 150 I=2,NA	
1099	IJ=IJ+INC	
1100 1101	IM1=I-1 DO 140 J=1,IM1	
1102 1103	IJ=IJ-INC AIJ=A(IJ)	
1104 1105	IF(AI).E0.ZER0) G0 T0 140 CALL DAXPY(MB,AIJ,B(I,1).MA,AB(J,1).NAB)	
1106 1107 14	CALL DAXPY(MB,AIJ,B(J,1),NA,AB(I,1),NAB) O CONTINUE	
1108 15 1109	0 CONTINUE RETURN	
1110	END	_
Top-dow	n view Bottom-up view Flat view	



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mthlib.f 🕄	
1053 END 1054 C	
1035 C*MODULE MTHLDTE *TAEKR MARAR MD.28, NAB, INCA	
1057 C	
1059 C 1060 IMPLICIT DOUBLE PRECISION(A+H,O-Z)	
1061 C 1062 DIMENSION A(*), B(NA,MB), AB(NAB,MB)	
1063 C 1064 PARAMETER (ZERO=0.0D+00) 1065 C	
1066 C* 31 0CT 1979 1067 C*	
1068 C*FUNCTION - TO MULTIPLY SYMMETRIC MATRIX A 1069 C* TIMES RECTANGULAR MATRIX B AND GET RECTANGULAR MATRIX AB	
1070 C++AAMMETERS	
1072 C* A - THE INPUT REAL SYMMETRIC MATRIX OF ORDER NA 1073 C* STORED IN SYMMETRIC STOAGE MODE.	
1073C* B - THE INPUT REAL NA BY MB RECTANGULAR MATRIX 1073C* NA - THE GNDER OF MATRIX A	
1076 KMB - THE COLUMN DIMENSION OF MATRICES B AND AB 1077 CF AB - THE OUTPUT PRODUCT NA BY MB MATRIX	
1078 C* NAB - THE INPUT ROW DIMENSION OF MATRIX AB 1079 C* INCA - ADDRESS DIFFERENCE OF TWO ADJACENT ELEMENTS OF A	
1080 C* 1081 INC=INCA	
1082 C 1083 C PROCESS DIAGONAL ELEMENTS OF INPUT MATRIX A 1084 C	
1005 IJ=1-INC 1086 DO 120 I=1,NA	
1087 1J=1/-1/1/I/C 1088 AIJ=A(1)	
1069 DO 110 K=1.MB 1090 AB(I,K)=AIJ*B(I,K)	
1091 110 CONTINUE 1092 120 CONTINUE	
1093 IF(NA.EO.1) RETURN 1094 C	
1000 C. BROCECC DE DIACONAL ELEMENTS DE TNULT MATOTY A	
1097 IJ=1-INC 1098 DO 150 I=2,NA	
1099 IJ=IJ+INC 1100 IM1=I-1	
1101 D0 140 J=1,IM1 1102 IJ=IJ+INC	
1103 AIJ=A(IJ) 1104 IF(AIJ.E0.ZERO) GO TO 140 1205 CALL DAXPY(MB.AIJ.B(I,1).NA.AB(J,1).NAB)	
1105 CALL DAXPY(MB,AIJ,B(I,1),NA,AB(J,1),NAB) 1106 CALL DAXPY(MB,AIJ,B(J,1),NA,AB(I,1),NAB) 1107 140 CONTINUE	
100 150 CONTINUE 100 RETURN	
110 END	
Top-down view Bottom-up view Flat view	



hpcviewer File View Filter	Help		
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1096 C			
1097		IJ=1-INC	
1098		DO 150 I=2,NA	
1099		IJ=IJ+INC	
1100		IM1=I-1	
1101		DO 140 J=1,IM1	
1102		IJ=IJ+INC	
1103		AIJ=A(IJ)	
1104		IF(AIJ.EQ.ZERO) GO TO 140	
1105		CALL DAXPY(MB,AIJ,B(I,1),NA,AB(J,1),NAB)	
1106		CALL DAXPY(MB,AIJ,B(J,1),NA,AB(I,1),NAB)	
1107	140	CONTINUE	
1108	150	CONTINUE	
1109		RETURN	
1110		END	
Top-down view Botto	om-up view Flat	view	



Case Study: Quicksilver

- Proxy application that represents some elements of LLNL's Mercury workload
- Solves a simplified dynamic Monte Carlo particle transport problem
 - Attempts to replicate memory access patterns, communication patterns, and branching or divergence of Mercury for problems using multigroup cross sections
- Parallelization: MPI, OpenMP, and CUDA
- Performance Issues
 - load imbalance (for canned example)
 - latency bound table look-ups
 - a highly branchy/divergent code path
 - poor vectorization potential



Quicksilver: Detailed analysis within a Kernel using PC Sampling

hpcviewer

File View Filter Help

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Profile	s
main.cc	ollisionEvent.cc ×
70	: uniqueNumber = monteCarlo->_materialDatabase->_mat[globalMatIndex]iso[isoIndex]gid; : numReacts = monteCarlo->_nuclearData->getNumberReactions(uniqueNumber); : (int reactIndex = 0; reactIndex < numReacts; reactIndex++)
73	currentCrossSection -= macroscopicCrossSection(monteCarlo, reactIndex, mc_particle.domain, mc_particle.cell,
74 75 76	<pre>isoIndex, mc_particle.energy_group); if (currentCrossSection < 0) {</pre>
77 78	<pre>selectedIso = isoIndex; selectedUniqueNumber = uniqueNumber;</pre>
30	selectedReact = reactIndex; break;
81 82	
83 } 84 qs_ 85	ert(selectedIso != -1);

Top-down view Bottom-up view Flat view

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Scope			NS: Sum (GINS:STL_ANY: Sum				GINS:STL_IFET: Sum		GINS:STL_IDEP:
<pre>4 14 » [1] cudaLaunchKernel<char></char></pre>	1.30e+11	100.0%			1.190+11	100.0%			5.2/e+09	100.0%			9.3464
▲ 211 » cudaLaunchKernel [qs]	1.30e+11	100.0%			1.19e+11	100.0%			5.27e+09	100.0%			9.34e+
⊿ » <gpu kernel=""></gpu>	1.30e+11	100.0%			1.19e+11	100.0%			5.27e+09	100.09	ê.		9.34e+
A » CycleTrackingKernel(MonteCarlo*, int, ParticleVault*, ParticleVau	1.30e+11	100.0% 4.	08e+07	0.0%	1.19e+11	100.0%	3.62e+07	0.0%	5.27e+09	100.09	2.11e+07	0.4%	9.34e+
▲ 132 » CycleTrackingGuts(MonteCarlo*, int, ParticleVault*, Particle	1.30e+11	100.0% 9.	03e+09	7.0%	1.19e+11	100.0%	9.01e+09	7.6%	5.24e+09	99.5%	8.98e+06	0.2%	9.32e+
# 26 >> [I] CycleTrackingFunction(MonteCarlo*, MC_Particle&, int, P	8.36e+10	64.4% 4.	12e+08	0.3%	7.25e+10	61.1%	3.65e+08	0.3%	5.21e+09	98.9%	1.02e+08	1.9%	9.25e4
loop at CycleTracking.cc: 118	8.35e+10	64.3% 3.	76e+08	0.3%	7.25e+10	61.1%	3.34e+08	0.3%	5.21e+09	98.8%	9.90e+07	1.9%	9.24et
▲ 63 ∞ CollisionEvent(MonteCarlo*, MC_Particle&, unsigned int) [5.20e+10	40.1% 4.	99e+09	3.8%	4.44e+10	37.4%	4.02e+09	3.4%	3.85e+09	73.1%	4.89e+08	9.3%	6.37e4
Ioop at CollisionEvent.cc: 67	4.09e+10	31.5% 8.	15e+08	0.6%	3.42e+10	28.8%	6.54e+08	0.6%	3.54e+09	67.1%	1.27e+08	2.4%	5.67e+
loop at CollisionEvent.cc: 71	3.85e+10	29.6% 2.	70e+09	2.1%	3.22e+10	27.1%	2.06e+09	1.7%	3.27e+09	62.0%	2.28e+08	4.3%	5.33e+
# 73 = macroscopicCrossSection(MonteCarlo*, int, int, i	3.58e+10	27.5% 1.	22e+10	9.4%	3.01e+10	25.4%	9.85e+09	8.31	3.04e+09	57.74	1.79e+09	33.91	4.600
▲ 41 ≫ NuclearData::getReactionCrossSection(unsigned int, u	2.09e+10	16.1% 1.	09e+10	8.4%	1.79e+10	15.1%	9.42e+09	7.9%	1.26e+09	23.8%	6.68e+08	12.7%	2.19e
253 w [I] NuclearDataReaction::getCrossSection(unsigned	6.89e+09	5.3% 3.	77e+09	2.9%	5.86e+09	4.9%	3.32e+09	2.8%	2.25e+08	4.39	8.24e+07	1.6%	8.86e+
NuclearData.cc: 253	6.28e+09	4.8% 6.	28e+09	4.8%	5.66e+09	4.8%	5.66e+09	4.8%	4.76e+08	9.0%	4.76e+08	9.0%	6.11e+
NuclearData.cc: 251	1.85e+09	1.4% 1.	85e+09	1.4%	1.64e+09	1.4%	1.64e+09	1.4%	8.12e+07	1.5%	8.12e+07	1.5%	2.47e+
NuclearData.cc: 248	1.61e+09	1.2% 1.	61e+09	1.2%	1.18e+09	1.0%	1.18e+09	1.0%	1.10e+08	2.1%	1.10e+08	2.1%	3.62e+
252 » [I] qs_vector <nucleardataspecies>::operator[](int)</nucleardataspecies>	1.29e+09	1.0% 1.	29e+09	1.0%	1.14e+09	1.0%	1.14e+09	1.0%	7.37e+04	0.09	7.37e+04	0.0%	1.24e4
NuclearData.cc: 252	1.12e+09	0.9% 1.	12e+09	0.9%	9.48e+08	0.8%	9.48e+08	0.8%	3.44e+05	0.0%	3.44e+05	0.0%	2.50et
252 » [I] qs_vector <nucleardatareaction>::size() const</nucleardatareaction>	9.41e+08	0.7% 9.	41e+08	0.7%	8.17e+08	0.7%	8.17e+08	0.7%					4.63e+
A DED _ ET	2.10-100	0.05.0		0. 30	2 47+100	0.00	2 434100			0.70	1 41++00	3 76	7 . 77.



Quicksilver: Detailed analysis within a Kernel using PC Sampling

Scope
▲ 14 » [1] cudaLaunchKernel <char></char>
✓ 211 » cudaLaunchKernel [qs]
⊿ » <gpu kernel=""></gpu>
A » CycleTrackingKernel(MonteCarlo*, int, ParticleVault*, ParticleVau
132 » CycleTrackingGuts(MonteCarlo*, int, ParticleVault*, Particle
▲ 26 » [I] CycleTrackingFunction(MonteCarlo*, MC_Particle&, int, P
Ioop at CycleTracking.cc: 118
▲ 63 » CollisionEvent(MonteCarlo*, MC_Particle&, unsigned int) [
▲ loop at CollisionEvent.cc: 67
▲ loop at CollisionEvent.cc: 71
▲ 73 ≫ macroscopicCrossSection(MonteCarlo*, int, int, i
41 >> NuclearData::getReactionCrossSection(unsigned int, u
253 » [I] NuclearDataReaction::getCrossSection(unsigned
NuclearData.cc: 253
NuclearData.cc: 251
NuclearData.cc: 248
252 » [I] qs_vector <nucleardataspecies>::operator[](int)</nucleardataspecies>
NuclearData.cc: 252
<pre>> 252 >> [I] qs_vector<nucleardatareaction>::size() const</nucleardatareaction></pre>



HPCToolkit Resources

- Documentation
 - -User manual for HPCToolkit: http://hpctoolkit.org/manual/HPCToolkit-users-manual.pdf
 - -Cheat sheet: https://gitlab.com/hpctoolkit/hpctoolkit/-/wikis/HPCToolkit-cheat-sheet
 - -User manual for hpcviewer: https://hpctoolkit.gitlab.io/hpcviewer
 - -Tutorial videos
 - http://hpctoolkit.org/training.html
 - recorded demo of GPU analysis of Quicksilver: https://youtu.be/vixa3hGDuGg
 - recorded tutorial presentation including demo with GPU analysis of GAMESS: https://vimeo.com/781264043
- Software
 - -Download hpcviewer GUI binaries for your laptop, desktop, cluster, or supercomputer
 - OS: Linux, Windows, MacOS
 - Processors: x86_64, aarch64, ppc64le
 - http://hpctoolkit.org/download.html
 - -Install HPCToolkit on your Linux desktop, cluster, or supercomputer using Spack
 - http://hpctoolkit.org/software-instructions.html

Some Hpcviewer Tips



Information for Using Hpcviewer

- Filtering GPU traces
 - · Can use the filter menu to select what execution traces you want to see
 - cpu only, gpu, a mix
 - type a string or a regular expression in the chooser select or unselect the new set
 - only traces that exceed a minimum number of samples
- · Filtering GPU calling context tree nodes to hide clutter
 - hide individual CCT nodes: e.g. lines that have no source code mapping library@0x0f450
 - hide subtrees: MPI implementation, implementation of CUDA primitives
- · When inspecting GPU activity, be aware that hpcviewer has two modes
 - · expose GPU traces or not
 - means: when displaying GPU trace lines, don't just show GPU activity if the time in the middle of a pixel is in a GPU operation. instead, show the first (if any) GPU operation between the time in the middle of the pixel and the middle of the next pixel
 - why? GPU activity is so short, it may be hard to find if we don't "expose" where it is
 - downside: makes the GPU appear more active than it is
 - · can correct the statistics by turning the mode off
 - mode can be selected from <File>:<Preferences>:<Traces>



Hands-on Examples



Two Kinds of Hands-on Examples

- Pre-collected databases to explore
 - gain experience using hpctoolkit's hpcviewer graphical user interface to analyze performance data
- Hands-on examples
 - build, run, and view several codes to get the full experience
 - hpcrun: measure an application as it executes
 - hpcstruct: recover program structure information for mapping measurements to source code
 - hpcprof: combine measurements with program structure information
 - hpcviewer: explore profiles and traces



Performance Databases to Explore

On an aurora login node

```
% qsub -I -l select=2,walltime=1:00:00,place=scatter -l filesystems=flare -A gpu_hack -q gpu_hack_prio -X
```

On an aurora compute node

% module use /soft/perftools/hpctoolkit/modulefiles

% module load hpctoolkit

% cd /flare/gpu_hack/hpctoolkit/data

% ls

arborx gamess minitest pelelmex quicksilver

NOTE: all of the databases in these directories end with the suffix ".d". For the gamess examples, the hpctoolkit databases are one directory deeper, i.e. in subdirectories that begin with a number.



More about the Available Performance Databases

See /flare/gpu_hack/hpctoolkit/data for each of the following

- quicksilver: Monte Carlo particle transport proxy application (C++ + CUDA)
 - hpctoolkit-qs-gpu-cuda.d profile and trace on 4 CPUs + 4 GPUs
 - · hpctoolkit-qs-gpu-cuda-pc.d instruction-level measurements within kernels using PC sampling
 - EXERCISES
- pelelmex: Adaptive mesh hydrodynamics simulation code for low Mach number reacting flows (C++ + AMReX)
 - pelelmex.db -a large trace with load imbalance from 2025 NERSC hackathon run on 16 CPUs + 16 GPU
- gamess: General Atomic and Molecular Electronic Structure System (Fortran + OpenMP)
 - 1.singlegroup-unbalanced/hpctoolkit-gamess-1n-chol-noDS.d
 - 2.singlegroup-balanced/hpctoolkit-gamess-1n-chol-fix_load_balance_noDS.d/
 - 3.multigroup-unbalanced-mtarbr/hpctoolkit-gamess-5n.d/
 - 4.multigroup-balanced/hpctoolkit-gamess-5n-manualbalance.d/
 - 5.multigroup-unbalanced-pc/hpctoolkit-gamess-5n-pc.d/
 - 6.scale/hpctoolkit-gamess-22n-test.d/
- arborx (C++ + Kokkos)
- minitest (SYCL and OpenMP TARGET)



Hands-on Tutorial Examples on Aurora

% git clone https://gitlab.com/hpctoolkit/tutorial-examples

```
% cd hpctoolkit-tutorial-examples/gpu/intel
```

% ls

arborx.kokkos.sycl minitest.sycl minitest.omp

For your chosen example

- 1. cd to the example directory
- 2. source setup-env/aurora.sh # custom for each example
- 3. make build # build the code
- 4. make run # submit to the batch queue

wait until the hpctoolkit database ending in .d appears and you see the file log.run.done appear

5. make view # launch hpcviewer to explore the performance data collected



Hands-on Tutorial Example on Polaris: Quicksilver

- % git clone https://gitlab.com/hpctoolkit/tutorial-examples
- % cd hpctoolkit-tutorial-examples/gpu/nvidia/quicksilver.cuda
 - 1. source setup-env/polaris.sh # custom for each example
 - 2. make build # build the code
 - 3. make run # submit to the batch queue

wait until the hpctoolkit database hpctoolkit-qs.d appears and you see the file log.run.done appear

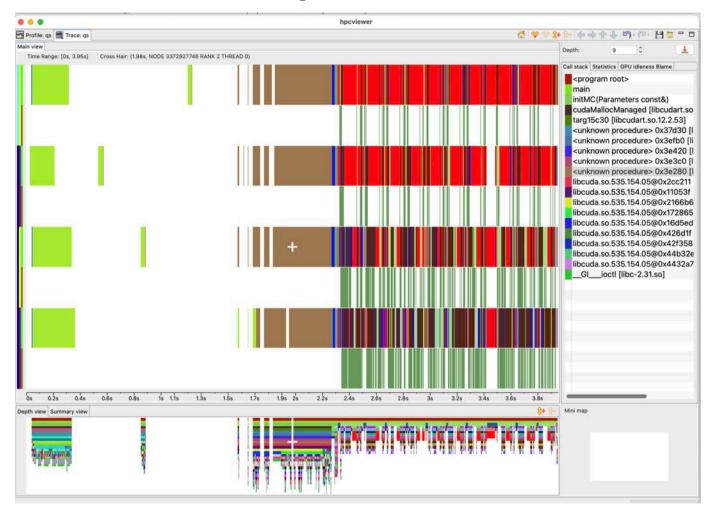
- 4. make view # launch hpcviewer to explore the profiles and traces
- 5. make run-pc # collect instruction-level kernel measurements
 wait until the hpctoolkit database hpctoolkit-qs-pc.d appears and you
 see the file log.run-pc.done appear
- 6. make view-pc # launch hpcviewer to explore the pc sampling data



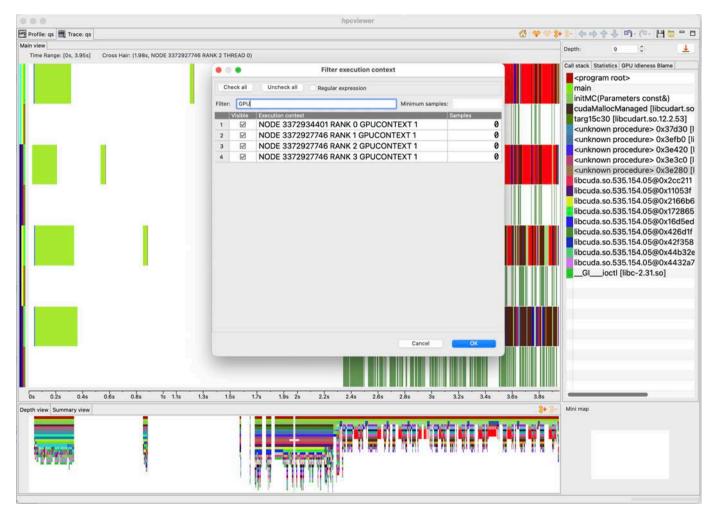
Inspecting the Pre-collected Quicksilver Data



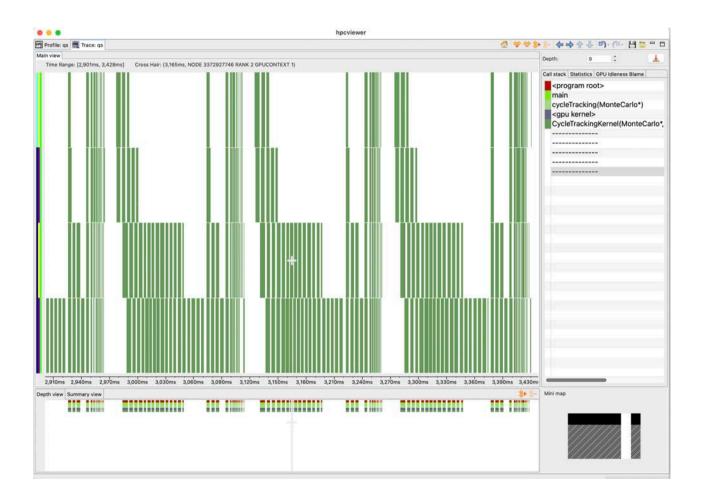
Select the Tab "Trace: qs"



Use the Filter to "Uncheck all" and Check "GPU" streams



See Load Imbalance Across the Four GPUs



Analyzing Quicksilver Traces

- Select the Trace tab "Trace: qs"
- Identifying the traces
 - · Select a pixel on a trace line
 - Look at legend on the top of the display, which reports the location of the "cross hair"
 - Is this a CPU or GPU trace line?
 - Repeat this a few times to identify what each of the trace lines represents
- Notice that each time you select a colored pixel on a trace line, you will be shown the function call stack in the rightmost pane
- At the top of the pane is a "depth" indicator, that indicates what level in the call stack you are viewing. The selected level will also be highlighted
- You can change the depth of your view by using the depth up/down, typing a depth, or simply selecting a frame in the call stack at the desired depth
- You can select 📥 above the call stack frame to show the call stacks at the deepest depth
 - If a sample doesn't have an entry at the selected depth, its deepest frame will be shown



Analyzing Quicksilver Traces

- Zoom in on a region in a trace by selecting it in the trace display
- Use the back button is to undo a zoom
- Use the control buttons
 - expand or contract the pane
 - move left, right, up, or down
- Keep an eye on the minimap in the lower right corner of the display to know what part of the trace you are viewing
- Use the home button de to reset the trace view to show the whole trace



Analyzing Quicksilver Traces

- Select the Trace tab "Trace: qs"
- Configure filtering
 - Use the Filter menu to select Filter Execution Contexts
 - In the filtering menu, select "Uncheck all"
 - Now, in the empty box preceded by "Filter:", type "GPU" and then click "Check all"
 - Select "OK".
 - Now, the Trace View will show only trace lines for the GPUs.
- · Inspect the trace data
 - Is the work load balanced across the GPUs? How can you tell?
 - Bring up the filter menu again. Select "Uncheck all". Type in "RANK 3" in the Filter box. Select thread 0 and the GPU context. Select "OK".
 - Move the call stack to depth 2
 - What CPU function is Rank 3 thread 0 executing when the GPU is idle?
 - Does this suggest any optimization opportunities?



Analyzing the Quicksilver Summary Profile

- Select the Profile Tab "Profile: qs"
- Use the column selector IV to deselect and hide the two REALTIME columns
- Select the GPU OPS column, which represents time spent in all GPU operations
- Select the **button to show the "hot path" according to the selected column**
 - the hot path of parent will continue into a child as long as the child accounts for 50% or more of the parent's cost
- The hot path will select "CycleTrackingKernel" a GPU kernel that consumes 100% of the GPU cost in this profile
- Use the III button to graph "GPU OPS (I)" inclusive GPU operations across the profiles
 - Are the GPU operations balanced or not across the execution contexts (ranks)?



Analyzing the Quicksilver Summary Profile

- You will notice that for quicksilver, HPCToolkit doesn't report any data copies between the host and device
 - The quicksilver code uses "unified memory" so that all of the data movement occurs between CPU and GPU using page faults rather than explicit copies
 - Today's GPU hardware doesn't support attribution of page faults to individual instructions
 - We could profile them, but not attribute them to code



The Profile View in the other "PC Sampling" Database

• • •	hpc	lewer						
Profile: qs 🗮 Trace: qs 🚾 Profile: qs								
CollisionEvent.cc ×								
<pre>67 for (int isoIndex = 0; isoIndex < 68 { 69 int uniqueNumber = monteCarlo 70 int numReacts = monteCarlon 71 for (int reactIndex = 0; react</pre>	>_materialDatabase->_mat[glo uclearData->getNumberReactio	balMatIndex ns(uniqueNu]iso[is	- 10 MAR	_gid;			
	niqueNumber;	lo, reactIn	ndex, mc_p	article.	domain, mc_	particle.	cell,	
pp-down view Bottom-up view Flat view								
Scope	in pression		GINS: Sum	Let.A	GINS:STL_ANY:	Contraction of the local division of the loc		Contraction of the Association o
Experiment Aggregate Metrics		e+11 100.05		100.0%	2.03e+11	and the second	2.03e+11	100.0%
<program root=""></program>		e+11 100.0	2		2.03e+11	100.0%		
 main 	2.1	e+11 100.05	8					
						100.0%		
 loop at main.cc: 66 		e+11 100.0			2.03e+11	100.0%		
4 58 » cycleTracking(MonteCarlo*)	2.15	e+11 100.05	*		2.03e+11 2.03e+11	100.0% 100.0%		
 58 » cycleTracking(MonteCarlo*) loop at main.cc: 232 	2.1	e+11 100.05 e+11 100.05	* *		2.03e+11 2.03e+11 2.03e+11	100.0% 100.0% 100.0%		
 58 » cycleTracking(MonteCarlo*) loop at main.cc: 232 loop at main.cc: 232 	2.1 2.1 2.1	e+11 100.0 ⁵ e+11 100.0 ⁵ e+11 100.0 ⁵	ક ક ક		2.03e+11 2.03e+11 2.03e+11 2.03e+11	100.0% 100.0% 100.0% 100.0%		
 58 » cycleTracking(MonteCarlo*) loop at main.cc: 232 loop at main.cc: 232 127 » <gpu kernel=""></gpu> 	2.1 2.1 2.1 2.1 2.1	e+11 100.05 e+11 100.05 e+11 100.05 e+11 100.05 e+11 100.05	જે જે જે		2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11	100.0% 100.0% 100.0% 100.0% 100.0%		
 58 » cycleTracking(MonteCarlo*) loop at main.cc: 232 loop at main.cc: 232 127 » <gpu kernel=""></gpu> * CycleTrackingKernel(MonteCarlo*, int, Part 	2.11 2.11 2.12 2.12 2.12 2.12 2.12 2.12	e+11 100.05 e+11 100.05 e+11 100.05 e+11 100.05 e+11 100.05	% % % % 1.03e+08	0	2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11	100.0% 100.0% 100.0% 100.0% 100.0%	9.83e+07	
 58 » cycleTracking(MonteCarlo*) loop at main.cc: 232 loop at main.cc: 232 loop at main.cc: 232 127 » <gpu kernel=""></gpu> % CycleTrackingKernel(MonteCarlo*, int, Part 132 » CycleTrackingGuts(MonteCarlo*, int,) 	2.11 2.12 2.12 2.12 2.12 2.12 2.12 2.12	e+11 100.09 e+11 100.09 e+11 100.09 e+11 100.09 e+11 100.09 e+11 100.09 e+11 99.99	% % % % 1.03e+08 % 2.04e+09	1.0%	2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11	100.0% 100.0% 100.0% 100.0% 100.0% 100.0% 99.9%	2.03e+09	1.09
 58 » cycleTracking(MonteCarlo*) loop at main.cc: 232 loop at main.cc: 232 127 » <gpu kernel=""></gpu> * CycleTrackingKernel(MonteCarlo*, int, Part 132 » CycleTrackingGuts(MonteCarlo*, int, 26 » [1] CycleTrackingFunction(MonteCarlo*) 	2.11 2.12 2.12 2.12 2.12 2.12 2.12 2.12	e+11 100.05 e+11 100.05 e+11 100.05 e+11 100.05 e+11 100.05 e+11 99.95 e+11 50.45	% % % % 1.03e+08 % 2.04e+09 % 4.95e+08	1.0%	2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 9.63e+10	100.0% 100.0% 100.0% 100.0% 100.0% 99.9% 47.5%	2.03e+09 4.38e+08	1.0 ⁹ 0.2 ⁹
 58 » cycleTracking(MonteCarlo*) loop at main.cc: 232 loop at main.cc: 232 127 » cgpu kernel> » CycleTrackingKernel(MonteCarlo*, int, Part 132 » CycleTrackingGuts(MonteCarlo*, int, I 26 » [I] CycleTrackingFunction(MonteCarlo*, int, I loop at CycleTracking.cc: 118 	2.11 2.12 2.12 2.12 2.12 2.12 2.12 2.12	e+11 100.05 e+11 100.05 e+11 100.05 e+11 100.05 e+11 100.05 e+11 99.95 e+11 50.45 e+11 50.45	* * * * * 1.03e+08 * 2.04e+09 * 4.95e+08 * 4.61e+08	1.0% 0.2% 0.2%	2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 9.63e+10 9.63e+10	100.0% 100.0% 100.0% 100.0% 100.0% 100.0% 99.9% 47.5% 47.5%	2.03e+09 4.38e+08 4.11e+08	1.09 0.29 0.29
 58 » cycleTracking(MonteCarlo*) loop at main.cc: 232 loop at main.cc: 232 127 » cgpu kernel> * » CycleTrackingKernel(MonteCarlo*, int, Part 132 » CycleTrackingFunction(MonteCarlo*, int, 26 » [I] CycleTrackingFunction(MonteCarlo*, MC_Part 63 » CollisionEvent(MonteCarlo*, MC_Part 	2.11 2.12 2.12 2.12 2.12 2.12 2.12 2.12	e+11 100.05 e+11 100.05 e+11 100.05 e+11 100.05 e+11 100.05 e+11 100.05 e+11 50.45 e+11 50.45 e+10 32.95	% % % % 1.03e+08 % 2.04e+09 % 4.95e+08 % 4.61e+08 % 7.69e+09	1.0% 0.2% 0.2% 3.6%	2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 9.63e+10 9.63e+10 6.21e+10	100.0% 100.0% 100.0% 100.0% 100.0% 100.0% 99.9% 47.5% 47.5% 30.7%	2.03e+09 4.38e+08 4.11e+08 6.42e+09	1.09 0.29 0.29 3.29
 58 » cycleTracking(MonteCarlo*) loop at main.cc: 232 loop at main.cc: 232 127 » cgpu kernel> » CycleTrackingKernel(MonteCarlo*, int, Part 132 » CycleTrackingGuts(MonteCarlo*, int, I 26 » [I] CycleTrackingFunction(MonteCarlo*, int, I loop at CycleTracking.cc: 118 	2.11 2.12 2.12 2.12 2.12 2.12 2.12 2.12	e+11 100.0% e+11 100.0% e+11 100.0% e+11 100.0% e+11 100.0% e+11 100.0% e+11 50.4% e+11 50.4% e+10 32.9% e+10 26.3%	<pre>% % % % % % % % % % % % % % % % % % %</pre>	1.0% 0.2% 0.2% 3.6% 0.7%	2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 9.63e+10 9.63e+10	100.0% 100.0% 100.0% 100.0% 100.0% 100.0% 99.9% 47.5% 47.5% 30.7%	2.03e+09 4.38e+08 4.11e+08	1.09 0.29 0.29 3.29
 58 » cycleTracking(MonteCarlo*) loop at main.cc: 232 loop at main.cc: 232 127 » <gpu kernel=""></gpu> * » CycleTrackingKernel(MonteCarlo*, int, Part 132 » CycleTrackingFunction(MonteCarlo*, int, 26 » [I] CycleTrackingFunction(MonteCarlo*, MC_Pate loop at CycleTracking.cc: 118 63 » CollisionEvent(MonteCarlo*, MC_Pate 	2.11 2.12 2.12 2.12 2.12 2.12 2.12 2.12	e+11 100.05 e+11 100.05 e+11 100.05 e+11 100.05 e+11 100.05 e+11 100.05 e+11 50.45 e+11 50.45 e+10 32.95	<pre>% % % % % % % % % % % % % % % % % % %</pre>	1.0% 0.2% 0.2% 3.6% 0.7%	2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 9.63e+10 9.63e+10 6.21e+10	100.0% 100.0% 100.0% 100.0% 100.0% 100.0% 99.9% 47.5% 47.5% 30.7% 24.1%	2.03e+09 4.38e+08 4.11e+08 6.42e+09	1.09 0.29 0.29 3.29 0.69
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 58 » cycleTracking(MonteCarlo*) loop at main.cc: 232 loop at main.cc: 232 loop at main.cc: 232 127 » <gpu kernel=""></gpu> % CycleTrackingKernel(MonteCarlo*, int, Part 132 » CycleTrackingGuts(MonteCarlo*, int, Part 26 » [I] CycleTrackingFunction(MonteCarlo*, int, loop at CycleTracking.cc: 118 63 » CollisionEvent(MonteCarlo*, MC_Pat loop at CollisionEvent.cc: 67 loop at CollisionEvent.cc: 71 	2.11 2.12 2.12 2.12 2.12 2.12 2.12 2.12	e+11 100.0% e+11 100.0% e+11 100.0% e+11 100.0% e+11 100.0% e+11 50.4% e+10 32.9% e+10 26.3% e+10 24.5%	<pre>% % % % % % % % % % % % % % % % % % %</pre>	1.0% 0.2% 0.2% 3.6% 0.7% 1.8% 8.3%	2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 9.63e+10 9.63e+10 6.21e+10 4.88e+10	100.0% 100.0% 100.0% 100.0% 100.0% 100.0% 99.9% 47.5% 47.5% 30.7% 24.1% 22.4% 20.9%	2.03e+09 4.38e+08 4.11e+08 6.42e+09 1.31e+09 3.08e+09	0.0% 1.0% 0.2% 0.2% 3.2% 0.6% 1.5% 7.3% 5.9%
 58 » cycleTracking(MonteCarlo*) loop at main.cc: 232 loop at main.cc: 232 loop at main.cc: 232 127 » <gpu kernel=""></gpu> % CycleTrackingKernel(MonteCarlo*, int, Part 132 » CycleTrackingGuts(MonteCarlo*, int, Part 26 » [I] CycleTrackingFunction(MonteCarlo*, int, et al. 26) loop at CycleTracking.cc: 118 63 » CollisionEvent(MonteCarlo*, MC_Pate loop at CollisionEvent.cc: 67 loop at CollisionEvent.cc: 71 73 » macroscopicCrossSection(MonteCarlo*, MonteCarlo*, MonteCar	2.11 2.12 2.13 2.14 2.15 2.11 2.12 2.14 2.15 2.11 2.12 2.12 2.12 3.04 2.12 2.12 2.12 2.12 3.04 3.04 3.04 4.04 4.04 4.04 5.66 5.27 teCarlo*, int, int, int, int, int, int) 4.83 sSection(unsigned int, unsigne 2.75	e+11 100.0% e+11 100.0% e+11 100.0% e+11 100.0% e+11 100.0% e+11 100.0% e+11 50.4% e+11 50.4% e+10 32.9% e+10 26.3% e+10 22.7% e+10 22.6%	<pre>% % % % % % % % % % % % % % % % % % %</pre>	1.0% 0.2% 0.2% 3.6% 0.7% 1.8% 8.3% 6.3%	2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 2.03e+11 9.63e+10 9.63e+10 6.21e+10 4.88e+10 4.54e+10 4.23e+10	100.0% 100.0% 100.0% 100.0% 100.0% 100.0% 99.9% 47.5% 47.5% 30.7% 24.1% 22.4% 20.9%	2.03e+09 4.38e+08 4.11e+08 6.42e+09 1.31e+09 3.08e+09 1.49e+10	1.09 0.29 0.29 3.29 0.69 1.59 7.39

Analyzing Quicksilver PC Samples

Using a measurement database with traces that was collected *with* PC sampling enabled

Using the default top-down view of the profile

- Select the column "GINS (I)" to focus on the measurement of inclusive GPU Instructions
- Select use the flame button to look at where the instructions are executed
- In the call stack revealed, you will <gpu kernel> placeholder that separates CPU activity (above) from GPU kernel activity (below)
- Below the <gpu kernel> placeholder you will see the function calls, inlined functions, loops and statements in HPCToolkit's reconstruction of calling contexts within the CycleTrackingKernel
- Using the bottom-up view of the profile
 - Select the bottom-up tab of above the control pane
 - Select the GINS STL_ANY (E) column, which will sort the functions by the exclusive GPU instruction stalls within that function
 - Scroll right to see which of the types of contributing types of stalls accounts for most of the STL_ANY amount
 - · Select the function that has the most exclusive stalls
 - Select the hot path to see where this function is called from.
 - Where do the calls to the costly function come from?
 - Does there appear to be an opportunity to reduce the number of calls to this function?



Filtering Tips to Hide Unwanted Implementation Details

- Filter "descendants-only" of CCT nodes with names *MPI* to hide the details of MPI implementation in profiles and traces
- Filter internal details of RAJA and SYCL templates to suppress unwanted detail using a "self-only" filter

Downloading, Installing, and Using Hpcviewer Graphical User Interface on Your Laptop







Hpcviewer Graphical User Interface on Your Laptop

Prepare to explore performance data on your laptop

- Download and install hpcviewer
 - See https://hpctoolkit.org/download.html

Select the right one for your laptop: MacOS (Apple Silicon, Intel), Windows, Linux Install a recent Java (17 or 21) if you don't have one, using the directions on the page

User manual for hpcviewer: https://hpctoolkit.gitlab.io/hpcviewer







Viewing Performance Data

- Copy a performance database directory to your laptop and open it locally
- Open a performance database on a remote system

Note: using a HPCViewer with a remote system presumes that hpcserver has already been installed on the remote system

- -hpcserver has been installed on Aurora
- you can download and install hpcserver on your local cluster as well (ask in Slack for directions)





Configuring Hpcviewer Remote Access

Run hpcviewer

- From the file menu, select "Open remote database"
- Fill in the hostname/IP address: aurora.alcf.anl.gov
- Fill in your username on Aurora
- Fill in the remote installation directory for hpcviewer's server: /soft/perftools/hpctoolkit/hpcserver
- Select the authentication method: "Use password"

Click "OK"

- Authenticate using your token as you normally do
- Navigate to a database with the file chooser in /flare/gpu_hack/hpctoolkit/data





Opening a Remote Database

é hpcviewer	File View Filter					0	(•)		Q	90	Tue 1	0:27 PM
	New window	96 N		hpcview	ver							
	Open database Open remote database		Open a remote database. The (hpcserver) has to be already	remote server launched.								

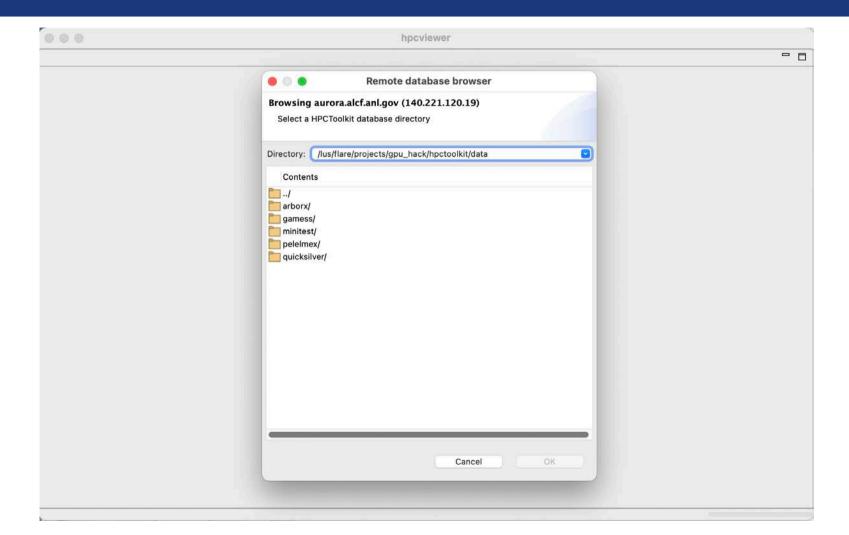
Configuring for remote access to Aurora using hpcserver

000	hpcviewer	
		- 8
	Remote connection Remote connection setup Enter the information needed to connect to the remote server	
	Hostname/IP address: aurora.alcf.anl.gov Username: johnmc	
	Use private key: /Users/johnmc/.ssh/id_rsa Use identity Proxy agent C	
	 O Use password ☑ SSH configuration: //Users/johnmc/.ssh/config ☑ Cancel OK 	

First View of Aurora: Your Home Directory

hpcviewer
🗧 💿 🔹 Remote database browser
Browsing polaris.alcf.anl.gov (140.221.112.14)
Select a HPCToolkit database directory
Directory: /home/johnmc
Contents
.allinea.polaris/ .allinea.thetagpu/ .cache/ .conda/ .config/ .dbus/ .emacs.d/ .fontconfig/ .gdb4hpc/ .gupg/ .hpstolkit/ .hpss/ .keras/ .lmod.d/ .local/ .nz/
Inv/ oracle_ire_usage/
Cancel OK

Navigate to Example Databases



Select a Quicksilver Database with Traces

	-
e e Remote database browser	
Browsing aurora.alcf.anl.gov (140.221.120.19) Select a HPCToolkit database directory	
Directory: /lus/flare/projects/gpu_hack/hpctoolkit/data/quicksilver	
Contents Contents/ hpctoolkit-qs-gpu-cuda-pc.d/	
hpctoolkit-qs-gpu-cuda.d/ EXERCISES	
Cancel	

After Selecting hpctoolkit-qs-gpu-cuda.d

		hpcviewer				
Profile: qs), s	- 1
p-down view Bottom-up view Flat view						_
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	I- 🗭					
		(I) REALTIME (sec): Sum (E)	GPUOP (sec): Sum (I)	GPUOP (sec): Sum (E)	GKER (sec):	Sum
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 [↑] → 	REALTIME (sec): Sum 1.58e+01 100	0.0% 1.58e+01 100.0%	₅ 2.23e+00 100.0	% 2.23e+00 100.0%	2.23e+00	10
	REALTIME (sec): Sum 1.58e+01 100	0.0% 1.58e+01 100.0%	₅ 2.23e+00 100.0	% 2.23e+00 100.0%	2.23e+00	10
 A[*] A[*] II Scope Experiment Aggregate Metrics 	REALTIME (sec): Sum 1.58e+01 100	0.0% 1.58e+01 100.0%	₅ 2.23e+00 100.0	% 2.23e+00 100.0%	2.23e+00	10
 A[*] A[*] II Scope Experiment Aggregate Metrics 	REALTIME (sec): Sum 1.58e+01 100	0.0% 1.58e+01 100.0%	₅ 2.23e+00 100.0	% 2.23e+00 100.0%	2.23e+00	10
 	REALTIME (sec): Sum 1.58e+01 100	0.0% 1.58e+01 100.0%	₅ 2.23e+00 100.0	% 2.23e+00 100.0%	2.23e+00	10
 	REALTIME (sec): Sum 1.58e+01 100	0.0% 1.58e+01 100.0%	₅ 2.23e+00 100.0	% 2.23e+00 100.0%	2.23e+00	10
 	REALTIME (sec): Sum 1.58e+01 100	0.0% 1.58e+01 100.0%	₅ 2.23e+00 100.0	% 2.23e+00 100.0%	2.23e+00	10
 	REALTIME (sec): Sum 1.58e+01 100	0.0% 1.58e+01 100.0%	₅ 2.23e+00 100.0	% 2.23e+00 100.0%	2.23e+00	10

Troubleshooting Tips



Why can't I see my Source Code?

- To relate performance measurements to your application source code, the code must be compiled with a "-g" option in addition to your optimization flags. Otherwise, there is no information for any tool to map performance to anything finer grain than at the procedure level
 - For instance, if you are building with make, you will want to build **RelWithDebInfo** rather than **Release** for the best experience

