

# Methods, Tools and Best Practices for Coupling Simulation and Al on ALCF Systems







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### **Overview**

- ☐ Motivation and methods for coupling AI and simulation
- ☐ Tools and examples on ALCF systems:
  - ➤ LibTorch for performant inference
  - ➤ ADISO2 for I/O and data streaming
  - ➤ In-memory distributed data-stores with SmartSim and DragonHPC
  - ➤ Use of node-local memory/storage
- ☐ Summary and best practices



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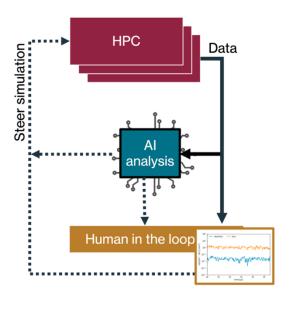
### Why Couple HPC Simulations with AI/ML?

- ☐ Substitute inaccurate or expensive components of simulation with ML models
  - > E.g.: Closure or surrogate modeling
- ☐ Optimize simulation parameters on-the-fly
  - > E.g.: Select solver parameters at runtime based on AI inference
- ☐ Avoid IO bottleneck and disk storage issues during offline training
  - > E.g.: In situ/online training through data streaming or in-memory staging
- ☐ Active learning and model fine-tuning
  - > E.g.: Continuous fine-tuning and deployment of model
  - > E.g.: Access training data not available during offline pre-training
- ☐ Steering of simulation ensembles
  - E.g.: Design space exploration or parameter optimization guided by AI



### How to Couple HPC Simulations and Al/ML?

**Steering of Ensembles** 



Buildo Data

Data

Data

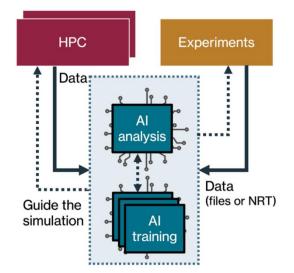
Trigger other simulations

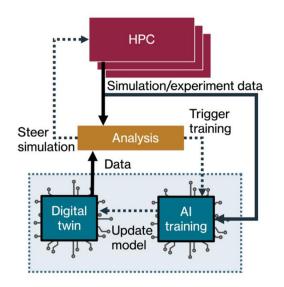
HPC simulation X

I part stage]

**Optimize Simulation Parameters** 

Active Learning/
Online Fine-Tuning





**Digital Twin** 

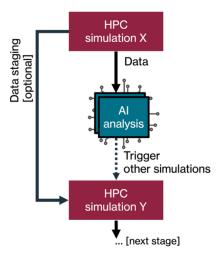


### How to Couple HPC Simulations and AI/ML?

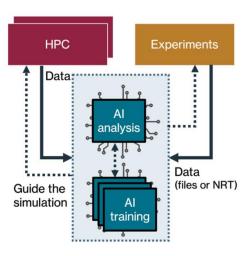
#### Type of Coupling

- ☐ ML inference only
- ☐ ML training only
- ☐ Both training and inference

#### **Inference Only**



#### **Training and Inference**



#### **Programming Languages and Programming Models**

- ☐ Simulation and ML components often written in different languages
- □ AI/ML relies heavily on vendor libraries (stuck with vendor language or programming model)
- ☐ Separate or shared parallelism strategies
  - ➤ Shared vs. separate MPI communicators
  - > Domain decomposition vs. batch or model parallelism



#### **Heterogeneous HPC node**

### How to Couple HPC Simulations and Al/ML?

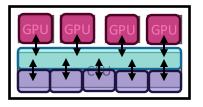
### GPU GPU GPU GPU

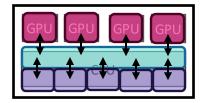
#### **Physical Proximity**

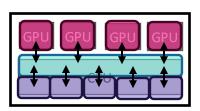
- ☐ Colocation: components share the same nodes
- ☐ Node-level clustering: components use different nodes on the same system
- ☐ Multi-system: components are run on separate specialized systems

## Simulation Database ML component Data transfer

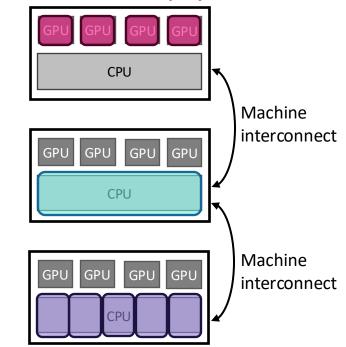
#### **Colocated Deployment**







#### **Node Clustered Deployment**



#### **System Clustered Deployment**





### How to Couple HPC Simulations and AI/ML?

#### **Data Access**

- ☐ Coupling simulation and ML requires frequent data sharing/transfer between components
- ☐ Direct: components share same memory space (may allow for zero-copy data transfer)
- ☐ Indirect: components use distinct logical memory (requires data copy and may require data transfer)

#### **Data Staging or Streaming**

- ☐ Staging: data is staged in memory or on disk (can reduce idle time but increases number of transfers)
- ☐ Streaming: data is streamed directly between components (can increase idle time due to synchronization)

#### **Heterogeneous HPC node**

### How to Couple HPC Simulations and Al/ML?

# GPU GPU GPU GPU CPU ML component Data transfer

#### **Execution Management**

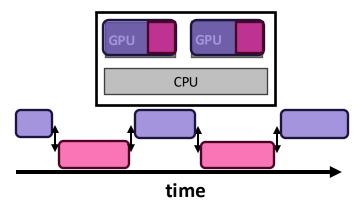
- ☐ Time division (tight coupling)
  - Components run on same compute resources (may even use same processes)
  - > Staggered in time, execution of one component halts the other
  - ➤ May allow for direct memory access and no data copy/transfer
  - ➤ Idle time of individual components may be significant

- ☐ Space division (loose coupling)
  - > Components run on separate compute resources
  - Concurrent in time, components run simultaneously
  - ➤ Minimal idle time of components for fast data copy/transfer
  - ➤ Usually requires indirect memory access with data copy/transfer

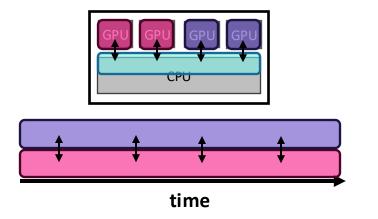
#### **Time Division: Same Compute Hardware**

Simulation

Database



#### **Space Division: Separate Compute Hardware**





### How to Couple HPC Simulations and Al/ML?

- ☐ Coupling simulation and AI/ML can be a complex space to navigate
- ☐ Implementation choices can vary significantly depending on ML task and application needs
- ☐ This talk will cover common tools and approaches supported at ALCF



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### Software Tools for Simulation-Al Coupled Workflows

☐ Software tools can be grouped by their capabilities

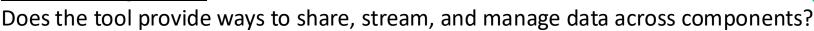
#### Task Launching or Kernel Execution



Does the tool enable execution of ML kernels on heterogeneous hardware?

Does the tool allow launching different components with fine-grained control on process placement?

#### **Data Management**







#### **Persistence Across Jobs**

Does the tool support execution across multiple jobs (e.g., by storing checkpoints)?





### Software Tools for Simulation-Al Coupled Workflows

☐ Software tools can be grouped by their capabilities or by execution management approach

#### **Tight Coupling**



Embedding Python or ML framework libraries into C, C++, Fortran simulation codes (e.g., PythonFOAM, TensorFlowFOAM, HONEE, and many others)





#### **Loose Coupling**

Workflow managers and client libraries which enable composition of workflows with multiple components, concurrency of execution, and data sharing/streaming





### PyTorch C++ API for Tightly-Coupled Inference

- ☐ PyTorch publicly exposes portions of the C++ API to provide support for
  - > Tensor and mathematical operations through Aten
  - Automatic differentiation through Autograd
  - > High-level API for training and inference of models, including TorchScript JIT compiled models
- ☐ Support for CPU, CUDA and Intel devices
  - > On Aurora, need Intel Extension for Torch to use Intel GPU until PyTorch 2.8 and for possible optimizations
- ☐ Ideally suited for performant inference from traditional simulation codes
- ☐ Available on Polaris and Aurora through ML Frameworks modules
- ☐ Documentation available for Polaris and Aurora with CMake build instructions and more examples





### PyTorch C++ API for Tightly-Coupled Inference

```
#include <torch/torch.h>
#include <torch/script.h>
// Select the device: CPU, CUDA or XPU
torch::DeviceType torch device;
if (torch::cuda::is available()) {
 torch device = torch::kCUDA;
} else if (torch::xpu::is_available()) {
 torch device = torch::kXPU;
} else {
 torch device = torch::kCPU;
// Load jit-traced model and offload to device
torch::jit::script::Module model;
model = torch::jit::load(argv[1]);
model.to(torch::Device(torch device));
// Create input tensor with specified dtype and device
auto options = torch::TensorOptions().dtype(torch::kFloat32).device(torch device);
torch::Tensor input tensor = torch::rand({1,3,224,224}, options);
// Perform inference
torch::NoGradGuard no grad; // equivalent to with torch.no grad()
torch::Tensor model output = model.forward({input tensor}).toTensor();
```





### PyTorch C++ API for Tightly-Coupled Inference

- ☐ Support for direct memory access on CPU and GPU through torch::from\_blob() (zero-copy view of data)
- ☐ On Aurora using SYCL

```
// Allocate inputs on device
sycl::queue Q;
Q = sycl::queue(sycl::gpu selector v);
float *d inputs = sycl::malloc device<float>(INPUTS SIZE, Q);
Q.fill(d inputs, 1.0f, INPUTS SIZE);
Q.wait();
// Create zero-copy view of inputs
auto options = torch::TensorOptions().dtype(torch::kFloat32).device(torch::kXPU);
torch::Tensor input tensor = torch::from blob(d inputs,{N BATCH,N FEATURES},options);
// Perform inference (always creates a new tensor)
torch::NoGradGuard no grad;
torch::Tensor model output = model.forward({input tensor}).toTensor();
// Copy outputs to SYCL array
float *d outputs = sycl::malloc device<float>(OUTPUTS SIZE, Q);
auto output tensor ptr = model output.contiguous().data ptr();
Q.memcpy((void *) d outputs, (void *) output tensor ptr, OUTPUTS SIZE*sizeof(float));
Q.wait();
```



### **ADIOS2 for I/O and Data Streaming**

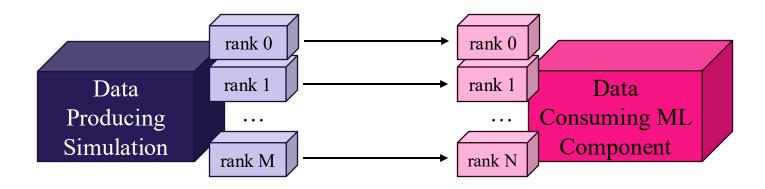


- □ ADIOS2 is a framework for I/O and streaming of scientific data
- ☐ Developed under U.S. DOE Exascale Computing Project and mostly maintained at OLCF
- □ APIs for traditional file system I/O and various data transport approaches (streaming, staging, zero-copy)
- ☐ Provide data streaming through Sustainable Staging Transport (SST) engine
- ☐ Support for C, C++, Fortran, and Python codes
- ☐ Modules available on both Polaris and Aurora, see our Documentation





- ☐ Classic data streaming architecture from producer to consumer (or multiple consumers)
- ☐ Designed for HPC with support for RDMA and MPI\*, but with fallback to Wide Area Network over sockets
- ☐ Supports MxN model, producer and consumer ranks don't have to match
- ☐ SST engine ideally suited for workflows with
  - ➤ Single data producer and uni-directional data flow
  - ➤ Low data-reuse (user must implement own data management otherwise)
  - > Fast and scalable data transfers needs across interconnect







☐ SST transport engine offers synchronous and asynchronous streaming

```
adios2::ADIOS adios(comm);
adios2::IO sstIO = adios.DeclareIO("solutionStream");
sstIO.SetEngine("Sst");
adios2::Params params;
if (mode == "sync") {
 params["RendezvousReaderCount"] = "1";
 params["QueueFullPolicy"] = "Block";
 params["QueueLimit"] = "1";
} else if (mode == "async") {
 params["RendezvousReaderCount"] = "0";
 params["QueueFullPolicy"] = "Discard";
 params["QueueLimit"] = "3";
 params["ReserveQueueLimit"] = "0";
params["DataTransport"] = "RDMA";
```

#### Sync mode

Producer waits for consumer to open stream
Producer is blocked if more than one step in queue
Useful if sequential execution of components wanted
Idle time on producer can be significant for slow consumer

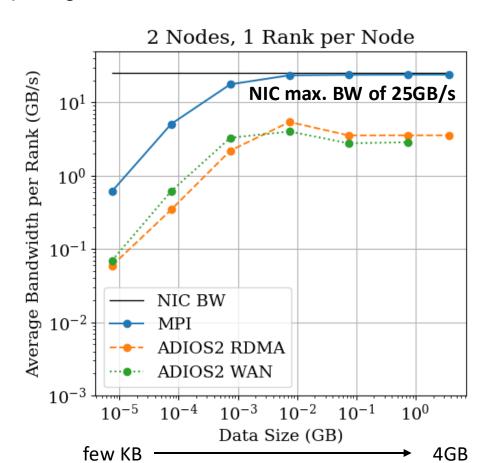
#### Async mode

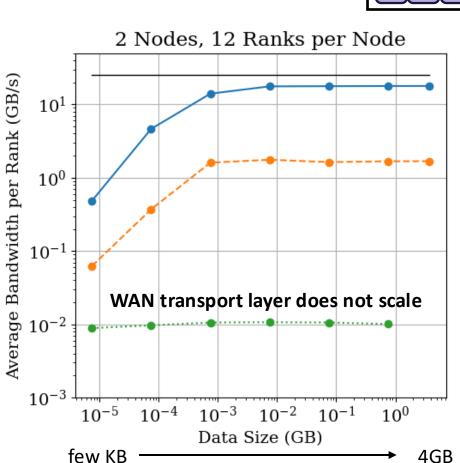
Producer proceeds even if consumer not available to read Producer discards writing step if queue is full and continues Enables concurrent execution of components with minimal idle time Steps are discarded with a slow consumer

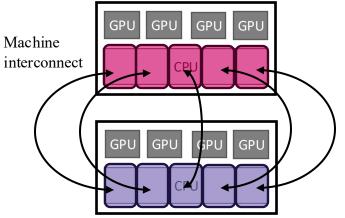
```
params["DataTransport"] = "RDMA";
params["OpenTimeoutSecs"] = "600"; // useful if consumer takes long to open reader stream
sstIO.SetParameters(params);
```



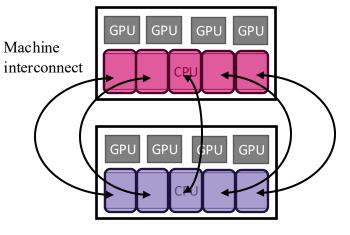
- ☐ SST streaming performance on Aurora with single producer, single consumer clustered example
- ☐ Comparing to MPI send/recv benchmark

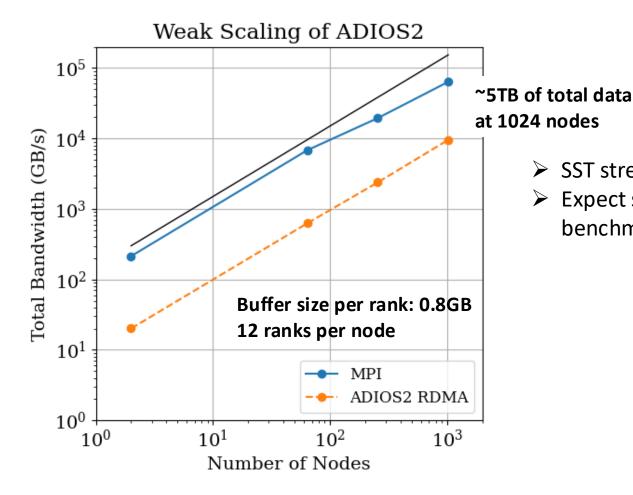






☐ SST streaming performance on Aurora with single producer, single consumer clustered example



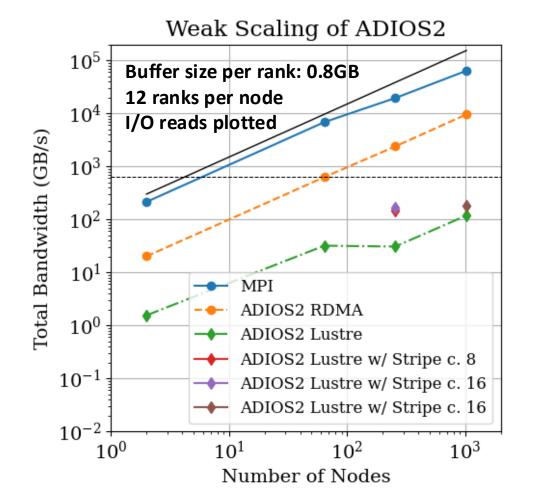


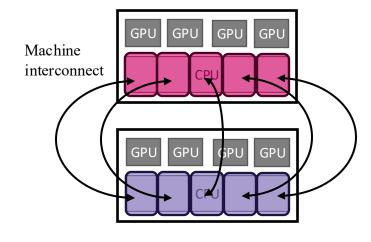
- SST streaming with RDMA is a scalable solution
- Expect similar performance to MPI send/recv benchmark with SST MPI transport and MPMD launch



### ADIOS2: SST vs. I/O Performance

☐ Comparison of SST RDMA to ADIOS2 I/O with BP5 format



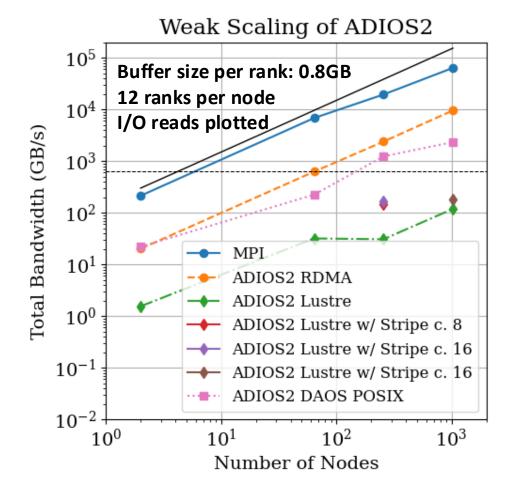


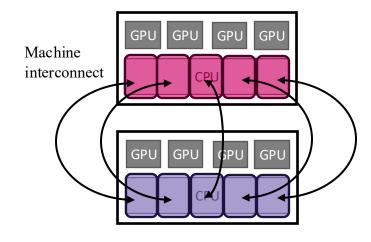
- SST streaming offers significant performance compared to Lustre I/O
- Increasing striping count can help with Lustre I/O



### ADIOS2: SST vs. I/O Performance

- ☐ Comparison of SST RDMA to ADIOS2 I/O with BP5 format
- ☐ Using DAOS with POSIX container



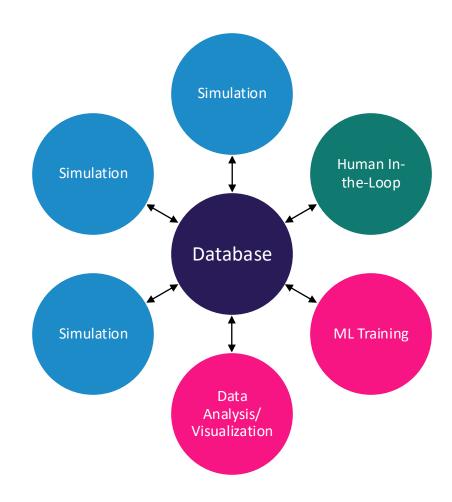


- ➤ ADIOS I/O with DAOS significantly improves performance over Lustre
- ➤ DAOS is great solution to combine performance with persistent storage of data transferred





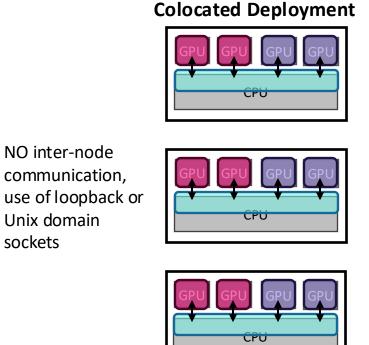
- ☐ Open source tool developed by HPE designed integrate traditional HPC simulations with ML
- □ <u>SmartSim</u> Infrastructure library (IL)
  - > Python API to manage workflow and launch HPC applications
  - ➤ Leverages distributed in-memory Redis database for staging and sharing data across components
- □ <u>SmartRedis</u> client library
  - > Python, C, C++, Fortran APIs to connect applications to database
  - > APIs for easy data management and model inference
- ☐ Hub and spoke model make SmartSim ideal for
  - ➤ Complex workflows with multiple producers and consumers
  - ➤ Need for data reuse or long-term staging
  - ➤ User interactivity and dynamism (can attach and detach components)
- ☐ See ALCF documentation to install SmartSim on Polaris and Aurora

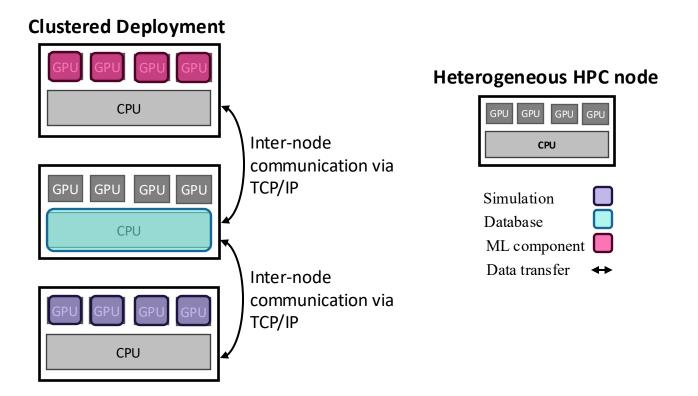






- ☐ SmartSim allows for clustered or colocated deployment of components
- ☐ Fine-grained control of process placement with CPU and GPU binding





NO inter-node

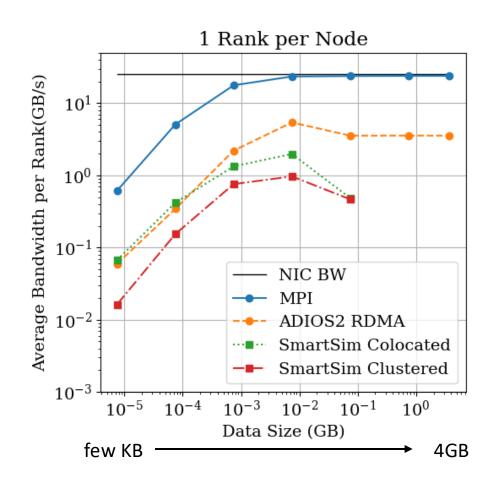
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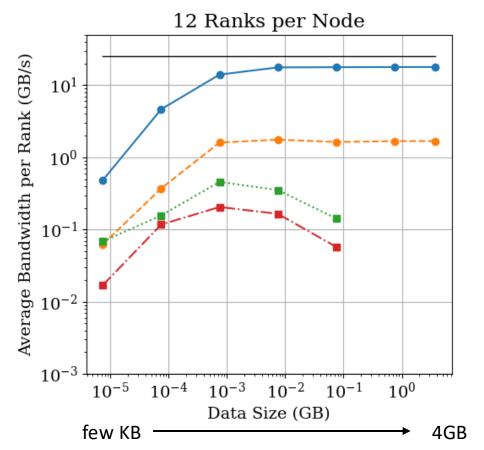
sockets

communication,



☐ SmartSim single producer, single consumer example on Aurora

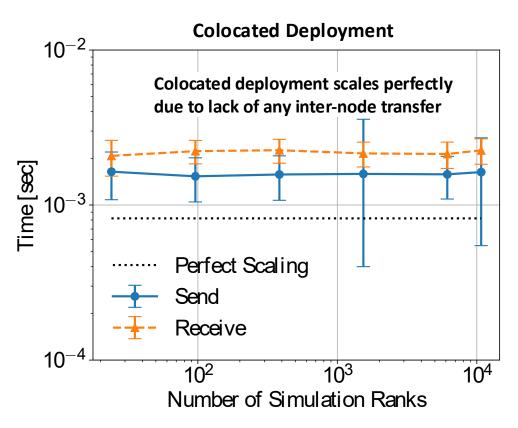


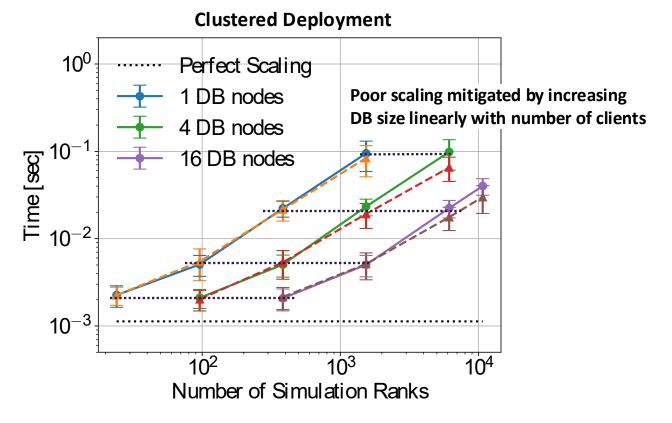






- ☐ SmartSim single producer, single consumer example
- ☐ Weak scaling on Polaris up to 448 nodes with 4 ranks per node
- ☐ Similar results for colocated deployment observed up to 2048 Aurora nodes







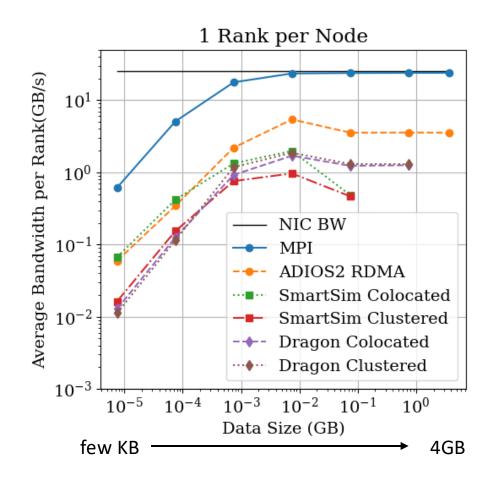


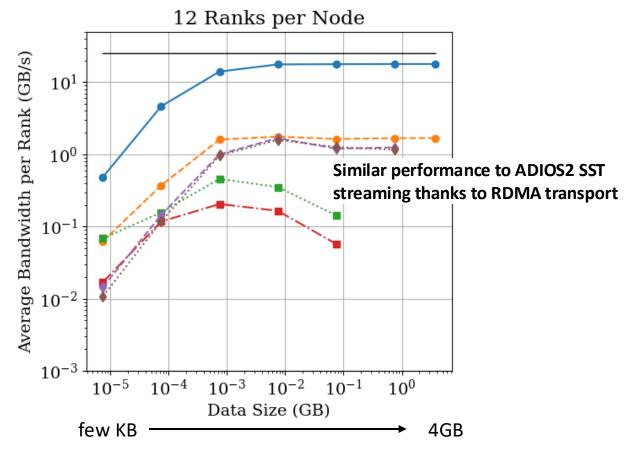
- □ <u>DragonHPC</u> is a composable distributed run-time for managing processes, memory, and data at scale through high-performance communication objects
- ☐ Open-source project developed by HPE
- ☐ Key features:
  - ➤ Multi-node extension to Python multiprocessing (mp.Process, mp.Pool, mp.Queue, ...)
  - ➤ Managed memory through sharded dictionary objects (DDict) available from Python and C++ clients
  - > Parallel process launching, including PMI enabled for MPI applications with fine-grained control of CPU/GPU affinity
  - > TCP and RDMA transport agents for inter-node communication
  - ➤ Live visualization of telemetry data, including hardware utilization and custom performance metrics
- ☐ DragonHPC is ideal for
  - > Extending Python workflows from local machine to HPC clusters
  - > Complex workflows with multiple producers and consumers
  - ➤ Need for data reuse or long-term staging thanks to DDict
  - ➤ Improved inter-node performance over SmartSim





☐ DragonHPC single producer, single consumer example (colocated and clustered deployment with DDict)

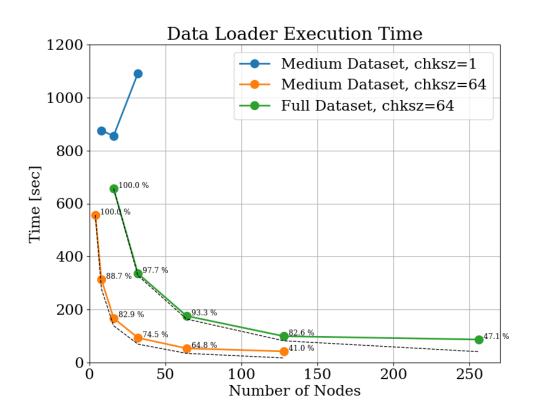


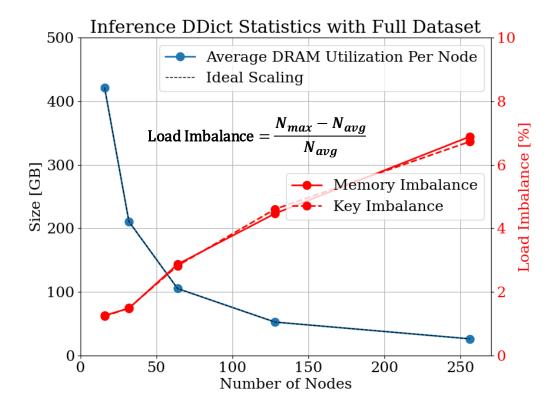






- ☐ Data loading of large datasets into distributed DDict with multi-node mp.Pool
  - ➤ Medium dataset: 1.5TB and 131,072 files
  - > Full dataset: 6TB and 500,354 files

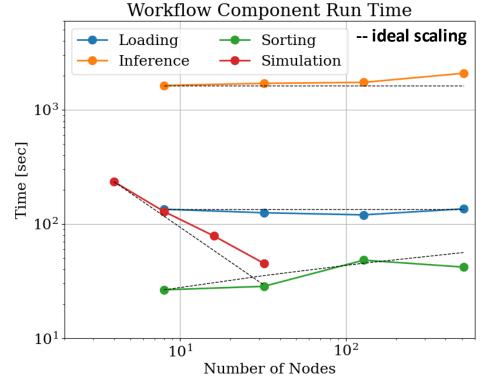




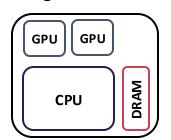


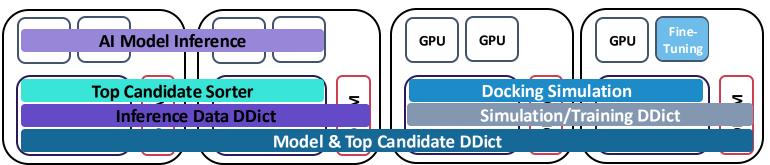


- ☐ Scalable workflow for fine-tuning AI surrogates for drug discovery
  - > Screen large compound dataset with Al model
  - ➤ Identify top candidate based on docking score
  - > Perform docking simulations of candidate compounds
  - > Fine-tune AI model
- ☐ Concurrently deploy components and DDict to enable data staging and re-use across iterations
- ☐ Colocate data with compute processes
- ☐ Workflow deployed up to 544 Aurora nodes



### Example of HPC system heterogeneous node

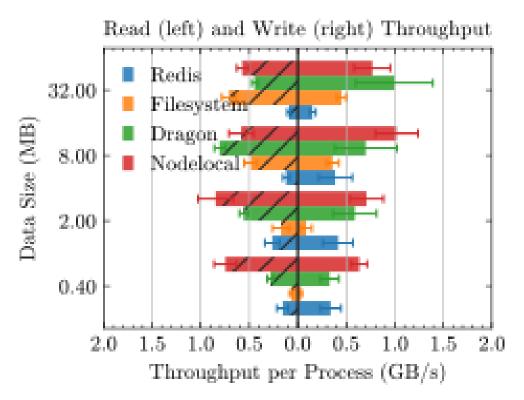






### **Node-Local Storage for Colocated Solutions**

- ☐ Workflows benefiting from colocated deployment solutions can use node-local storage or memory
- ☐ On Aurora, only DRAM is available through /tmp
- ☐ On Polaris, can use pair of 1.6TB SSDs through /local/scratch or DRAM through /tmp
- ☐ Provides fast I/O (especially small data sizes) and simple implementation





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### **Summary**

- □ Coupling AI with traditional HPC simulation can accelerate science campaigns, but deployment and integration can be challenging
- □ ALCF users can leverage various approaches and software tools to best fit their needs
- ☐ User guide to sim-Al coupled workflows
  - > Fast inference? Tight-coupling and direct memory access with PyTorch C++ API
  - ➤ Uni-directional data flow between large scale producer and consumer(s)? Streaming with ADIOS SST
  - > Combine data sharing during online workflow with persistent storage? Leverage DAOS on Aurora
  - > Complex, multi-component workflows with multiple producers and consumers? Use SmartSim or DragonHPC
  - > Staging of large datasets with data reuse? Leverage SmartSim database and DragonHPC Distributed Dictionary
  - ➤ Need integration with LLMs? Check out our <u>Inference Endpoints!</u>



### **Best Practices for Sim-Al Workflows on HPC Systems**

- ☐ Limit I/O and and parrel file system usage by streaming data across components
  - Remember DAOS on Aurora can be a performant solution!
- ☐ Ensure high data-reuse by staging relevant data in memory
- ☐ Ensure good resource utilization by leveraging available CPU & GPU in colocated deployments and exploiting concurrency of tasks when possible
  - ➤ Make use of CPUs, especially on Aurora!
- □ Achieve good scalability by reducing data transfer across network with colocation of data and compute or using fast streaming solutions





### Questions?

For further questions or issues, please reach out to ALCF support at <a href="mailto:support@alcf.anl.gov">support@alcf.anl.gov</a>

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### **User Guide to Sim-Al Coupled Workflows**

☐ Fast inference? Tight-coupling and direct memory access with PyTorch C++ API ☐ Uni-directional data flow between large scale producer and consumer(s)? Streaming with ADIOS SST ☐ Combine data sharing during online workflow with persistent storage? Leverage DAOS on Aurora ☐ Complex, multi-component workflows with multiple producers and consumers? Use SmartSim or DragonHPC ☐ Staging of large datasets with data reuse? Leverage SmartSim database and DragonHPC Distributed Dictionary ☐ Need support for multiple programming languages? Build workflow with SmartSim/SmartRedis ☐ Can leverage Python? Utilize DragonHPC □ Need GPU-direct data transfer? Use ADIOS2 □ Need scalability to 1000s of nodes? Colocated deployment of components with SmartSim or DragonHPC □ Need integration with LLMs? Check out our <u>Inference Endpoints!</u>

